



Winstar Display Co., LTD

華凌光電股份有限公司

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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF57FTLFFDANO#

<p style="text-align: center;">APPROVED BY:</p> <p>(FOR CUSTOMER USE ONLY)</p>	<p>PCB VERSION: _____</p> <p>DATA: _____</p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
			葉虹蘭
ISSUED DATE: 2014/12/24			

TFT Display Inspection Specification: <http://www.winstar.com.tw/service.php>

RECORDS OF REVISION

DOC. FIRST ISSUE

VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2013/09/26		First issue
A	2014/12/24		Add size & Surface. Modify Pixel Data Format ,nterface & Block Diagram.

Contents

- 1.Module Classification Information
- 2.Summary
- 3.General Specification
- 4.Absolute Maximum Ratings
- 5.Electrical Characteristics
- 6.DC Characteristics
- 7.Interface Timing Characteristics
- 8.Optical Characteristics
- 9.Interface
- 10.Block Diagram
- 11.Reliability
- 12.Contour Drawing
- 13.Package Specification
- 14.Initial Code For Reference

1.Module Classification Information

W F 57 F T L F F D A N 0 #
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨ ⑩ ⑪ ⑫ ⑬

①	Brand : WINSTAR DISPLAY CORPORATION						
②	Display Type : F→TFT Type, J→Custom TFT						
③	Display Size : 5.7" TFT						
④	Model serials no.						
⑤	Backlight Type :	F→CCFL, White S→LED, High Light White			T→LED, White		
⑥	LCD Polarize Type/ Temperature range/ Gray Scale Inversion Direction	C→Transmissive, N. T, 6:00 ; I→Transmissive, W. T, 6:00 F→Transmissive, N.T,12:00 ; L→Transmissive, W.T,12:00 Z→Transmissive, W.T, Wide Viewing Angle for O-FILM Y→Transmissive, W.T, Wide View					
⑦	A : TFT LCD B : TFT+FR+CONTROL BOARD C : TFT+FR+A/D BOARD D : TFT+FR+A/D BOARD+CONTROL BOARD E : TFT+FR+POWER BOARD F : TFT+CONTROL BOARD			G : TFT+FR H : TFT+D/V BOARD I : TFT+FR+D/V BOARD J : TFT+POWER BD			
⑧	Solution:						
	A: 128160	B:320234	C:320240	D:480234	E:480272	F: 640480	G: 800480
	H:1024600	I:320480	J:240320	K:800600	L:240400	M :1024768	P :1280800
⑨	D: Digital L : LVDS						
⑩	Interface : N : without control board A : 8Bit B : 16Bit						
⑪	TS : N : Without TS T : resistive touch panel C : capacitive touch panel						
⑫	Version						
⑬	Special Code	#:Fit in with ROHS directive regulations					

2.Summary

This technical specification applies to 5.7' color TFT-LCD panel. The 5.7' color TFT-LCD panel is designed for camcorder, digital camera application and other electronic products which require high quality flat panel displays. This module follows RoHS.

3.General Specifications

Item	Dimension	Unit
Size	5.7	inch
Dot Matrix	640 x RGBx480(TFT)	dots
Module dimension	125.0 x98.8 x8.3	mm
Active area	115.20 x 86.40	mm
Dot pitch	0.06 x 0.18	mm
LCD type	TFT, Normally White , Transmissive	
View Direction	6 o'clock	
Gray Scale Inversion Direction	12 o'clock	
Backlight Type	LED,Normally White	
Controller IC	SSD1963	
Interface	Digital 8080 family MPU	
With /Without TP	WithoutTP	
Surface	No Anti-Glare	

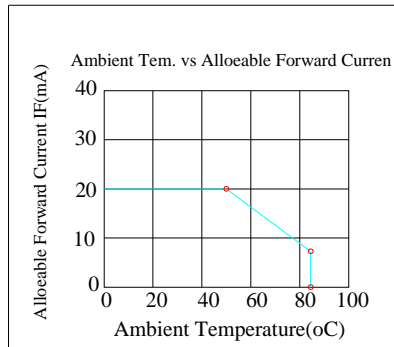
*Color tone slight changed by temperature and driving voltage.

4. Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

- Temp. $\leq 60^{\circ}\text{C}$, 90% RH MAX. Temp. $> 60^{\circ}\text{C}$, Absolute humidity shall be less than 90% RH at 60°C



5. Electrical Characteristics

5.1. Operating conditions:

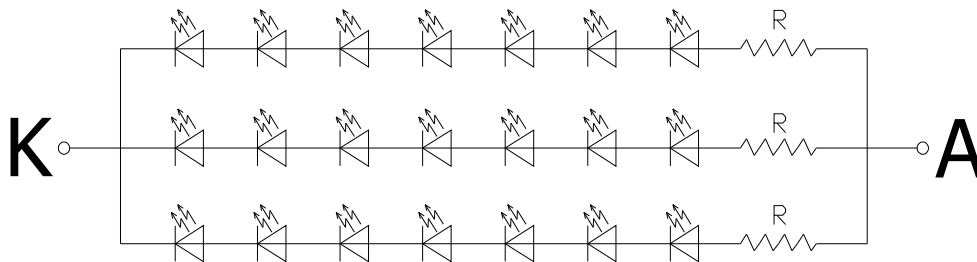
Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For LCM	V_{DD}	—	3.0	3.3	3.6	V
Supply Current For LCM	I_{DD}	—	—	120	190	mA

Note 1 : This value is test for $V_{DD}=3.3V$, $T_a=25\text{ }^{\circ}C$ only

5.2. LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	-	-	60	-	mA	-
Power Consumption	--	-	1386	1470	mW	-
LED voltage	V_{BL+}	-	23.1	24.5	V	Note 1
LED Life Time		-	25,000	-	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED



Note 2 : $T_a = 25\text{ }^{\circ}C$

Note 3 : Brightness to be decreased to 50% of the initial value

Note 4 : The single LED lamp case

6.DC CHARATERISTICS

Parameter	Symbol	Rating			Unit	Condition
		Min	Typ	Max		
Low level input voltage	V_{IL}	0	-	0.3VDD	V	
High level input voltage	V_{IH}	0.7VDD	-	VDD	V	

7. Interface timing

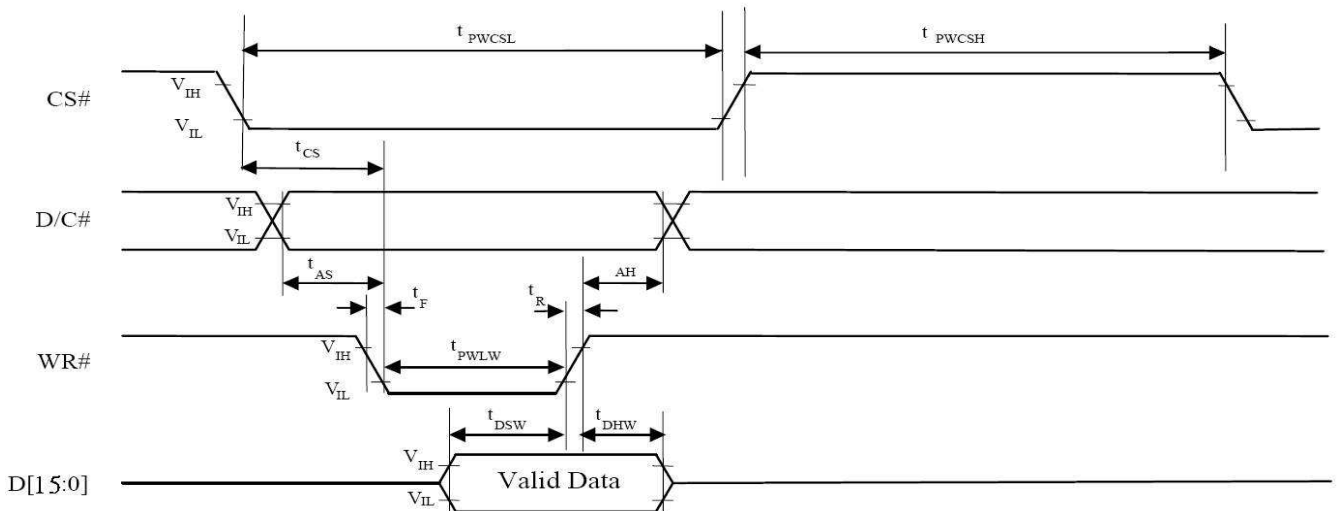
7.1. 8080 Mode

The 8080 mode MCU interface consist of CS#, D/C#, RD#, WR#, Data Bus . This interface use WR# to define a write cycle and RD# for read cycle. If the WR# goes low when the CS# signal is low, the data or command will be latched into the system at the rising edge of WR#. Similarly, the read cycle will start when RD# goes low and end at the rising edge of RD#.

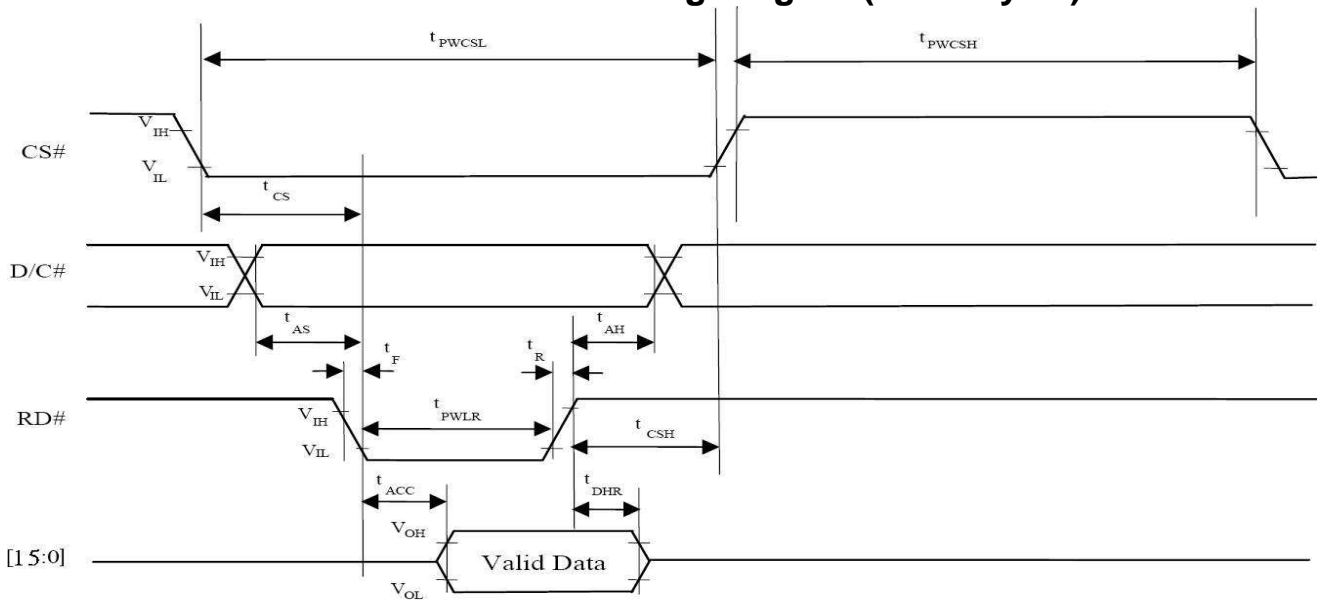
7.2. 8080 Mode Write Cycle

Symbol	Parameter	Min	Typ	Max	Unit																																																						
fMCLK	System Clock Frequency	1	-	110	MHz																																																						
tMCLK	System Clock Period	1/fMCLK	-	-	ns																																																						
tPWCSH	Control Pulse High Width Write	13	1.5* tMCLK	-	ns																																																						
	Read	30	3.5* tMCLK	-	ns																																																						
tPWCSL	Control Pulse Low Width Write (next write cycle)	13	1.5* tMCLK	-	ns																																																						
	Write (next read cycle)	80	9* tMCLK	-	ns																																																						
	Read	80	9* tMCLK	-	ns																																																						
tAS	Address Setup Time	1	-	-	ns																																																						
tAH	Address Hold Time	2	-	-	ns																																																						
tDSW	Write Data Setup Time	4	-	-	ns </tr <tr> <td>tDHW</td> <td>Write Data Hold Time</td> <td>1</td> <td>-</td> <td>-</td> <td>ns</td> </tr> <tr> <td>tPWLW</td> <td>Write Low Time</td> <td>12</td> <td>-</td> <td>-</td> <td>ns</td> </tr> <tr> <td>tDHR</td> <td>Read Data Hold Time</td> <td>1</td> <td>-</td> <td>-</td> <td>ns</td> </tr> <tr> <td>tACC</td> <td>Access Time</td> <td>32</td> <td>-</td> <td>-</td> <td>ns</td> </tr> <tr> <td>tPWLR</td> <td>Read Low Time</td> <td>36</td> <td>-</td> <td>-</td> <td>ns</td> </tr> <tr> <td>tR</td> <td>Rise Time</td> <td>-</td> <td>-</td> <td>0.5</td> <td>ns</td> </tr> <tr> <td>tF</td> <td>Fall Time</td> <td>-</td> <td>-</td> <td>0.5</td> <td>ns</td> </tr> <tr> <td>tCS</td> <td>Chip select setup time</td> <td>2</td> <td>-</td> <td>-</td> <td>ns</td> </tr> <tr> <td>tCSH</td> <td>Chip select hold time to read signal</td> <td>3</td> <td>-</td> <td>-</td> <td>ns</td> </tr>	tDHW	Write Data Hold Time	1	-	-	ns	tPWLW	Write Low Time	12	-	-	ns	tDHR	Read Data Hold Time	1	-	-	ns	tACC	Access Time	32	-	-	ns	tPWLR	Read Low Time	36	-	-	ns	tR	Rise Time	-	-	0.5	ns	tF	Fall Time	-	-	0.5	ns	tCS	Chip select setup time	2	-	-	ns	tCSH	Chip select hold time to read signal	3	-	-	ns
tDHW	Write Data Hold Time	1	-	-	ns																																																						
tPWLW	Write Low Time	12	-	-	ns																																																						
tDHR	Read Data Hold Time	1	-	-	ns																																																						
tACC	Access Time	32	-	-	ns																																																						
tPWLR	Read Low Time	36	-	-	ns																																																						
tR	Rise Time	-	-	0.5	ns																																																						
tF	Fall Time	-	-	0.5	ns																																																						
tCS	Chip select setup time	2	-	-	ns																																																						
tCSH	Chip select hold time to read signal	3	-	-	ns																																																						

7.3. Parallel 8080-series Interface Timing Diagram(Write Cycle)



7.4. Parallel 8080-series Interface Timing Diagram(Read Cycle)



7.5. Pixel Data Format

Interface	Cycle	D[15]	D[14]	D[13]	D[12]	D[11]	D[10]	D[9]	D[8]	D[7]	D[6]	D[5]	D[4]	D[3]	D[2]	D[1]	D[0]
16 bits (565 format)	1 st	R5	R4	R3	R2	R1	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1
16 bits	1 st	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0
	2 nd	B7	B6	B5	B4	B3	B2	B1	B0	R7	R6	R5	R4	R3	R2	R1	R0
	3 rd	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
8 bits	1 st									R7	R6	R5	R4	R3	R2	R1	R0
	2 nd									G7	G6	G5	G4	G3	G2	G1	G0
	3 rd									B7	B6	B5	B4	B3	B2	B1	B0

8. Optical Characteristics

Item	Symbol	Condition.	Min	Typ.	Max.	Unit	Remark	
Response time	Tr+ Tf	$\theta=0^\circ$ 、 $\Phi=0^\circ$	-	50	80	ms	Note 3,5	
Contrast ratio	CR	At optimized viewing angle	150	250	-	-	Note 4,5	
Color Chromaticity	White	$\theta=0^\circ$ 、 $\Phi=0^\circ$	Wx	0.25	0.3	0.35	-	Note 2,6,7
			Wy	0.27	0.32	0.37	-	
Viewing angle (Gray Scale Inversion Direction)	Hor.	$CR \geq 10$	Θ_R	-	60	-	Deg.	Note 1
			Θ_L	-	60	-		
	Ver.		Φ_T	-	60	-		
			Φ_B	-	40	-		
Brightness	-	-	250	300	-	cd/ m ²	Center of display	

Ta=25±2°C, IL=60mA

Note 1: Definition of viewing angle range

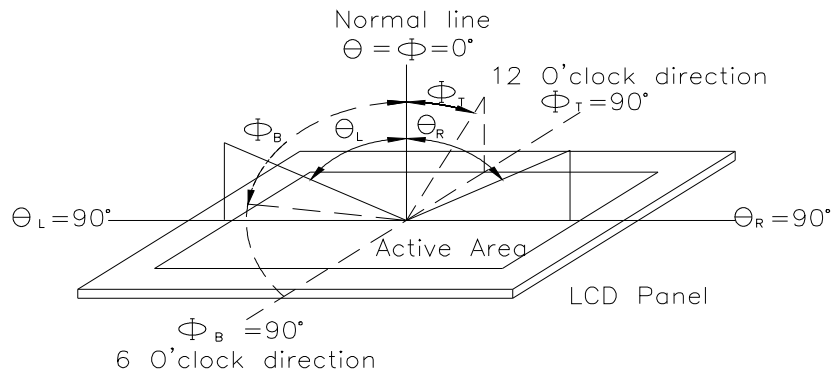


Fig.8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

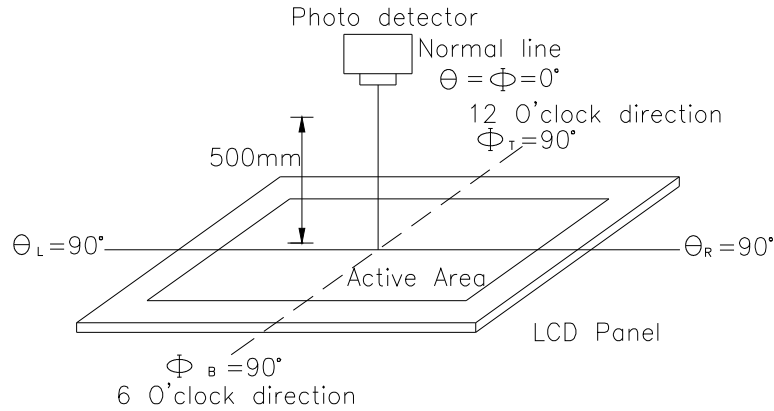
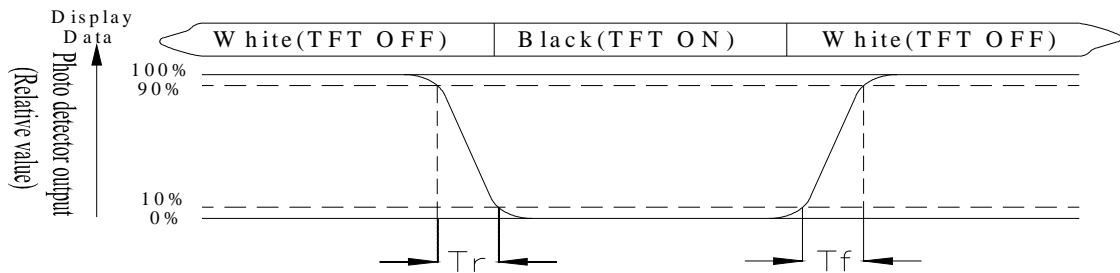


Fig. 8.2. Optical measurement system setup

Note 3: Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%.



Note 4: Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: White $V_i = V_{i50} \pm 1.5V$

Black $V_i = V_{i50} \pm 2.0V$

“±” means that the analog input signal swings in phase with VCOM signal.

“±” means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 6: Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

Note 7: Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

9.Interface

9.1. LCM PIN Definition

Pin No.	Symbol	Description	Remark
1	GND	System ground pin of the IC Connect to system ground	
2	VDD	Power Supply : +3.3V	
3	NC	No connection	
4	D/C	Data/Command select	
5	R/W	Read /Write strobe signal	
6	E	Enable signal	
7	DB0	Data bus	
8	DB1	Data bus	
9	DB2	Data bus	
10	DB3	Data bus	
11	DB4	Data bus	
12	DB5	Data bus	
13	DB6	Data bus	
14	DB7	Data bus	
15	CS	Chip select	
16	UD	Up/down selection	Note1
17	LR	Left /right selection	Note1
18	RST	Hardware reset	
19	NC	No connection	
20	NC	No connection	

Note 1: DISPLAY DIRECTION OF THE PANEL

The UD and LR control the Display direction of the panel .

The settings of UD and LR are or following:

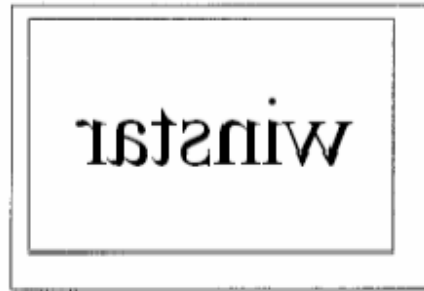
Note 2: Selection of scanning mode

Setting of scan control input		Scanning direction
UD	LR	
VDD	GND	Up to down, left to right
GND	VDD	Down to up, right to left
VDD	VDD	Up to down, right to left
GND	GND	Down to up, left to right

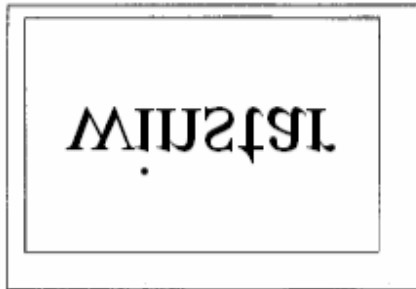
Note 3: Definition of scanning direction.Refer to the figure as below:



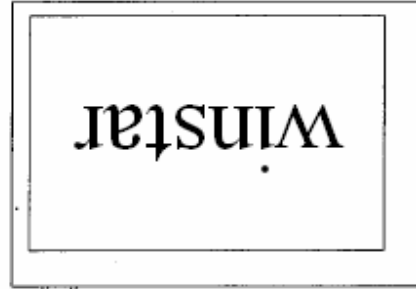
UD=VDD, LR=GND



UD=VDD, LR=VDD



UD=GND, LR=GND

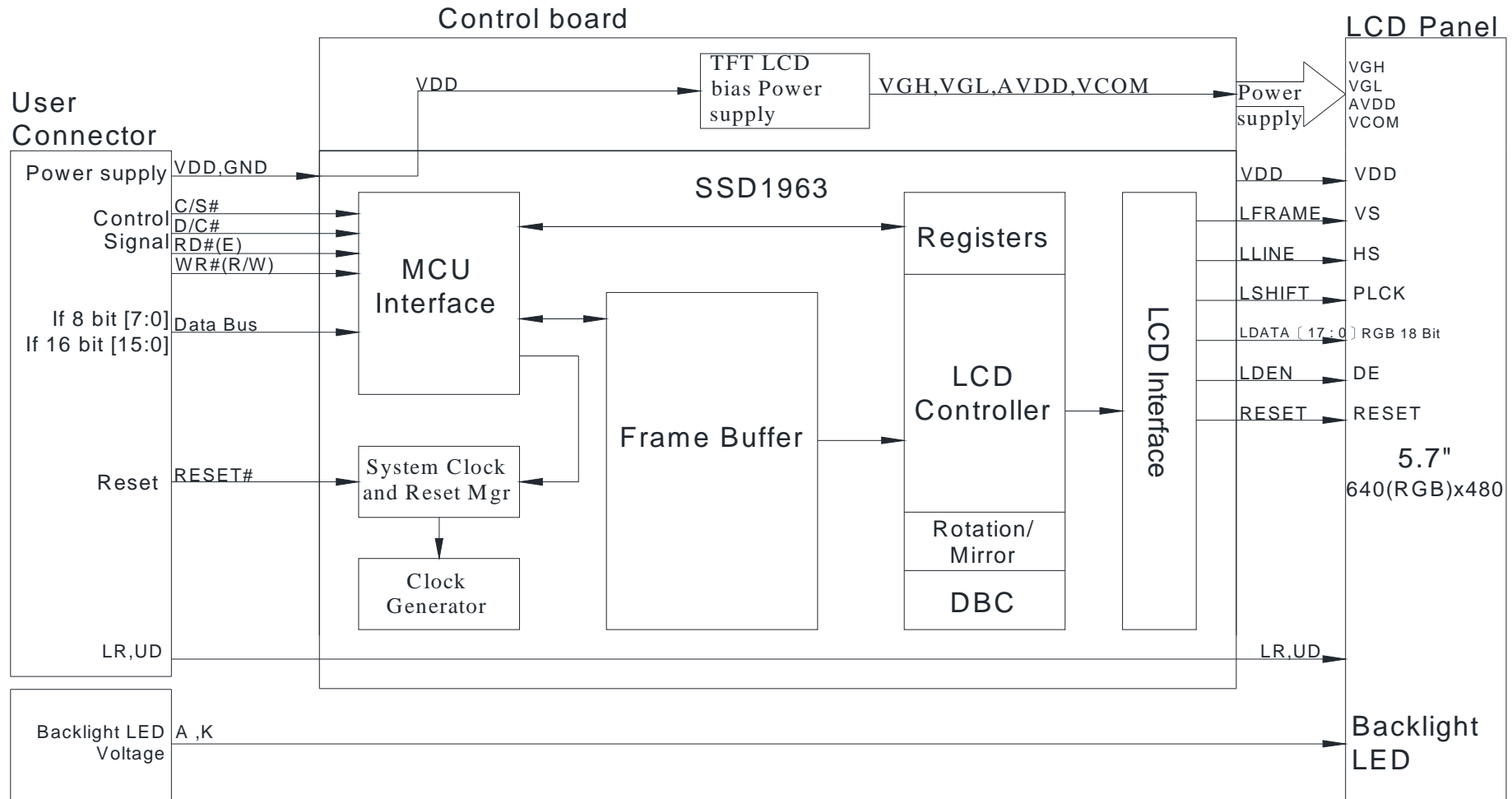


UD=GND, LR=VDD

9.2. Backlight PIN Definition: JST BHSR-02VS-1

Pin No.	Symbol	I/O	Description
1	A	I	LED_ Anode (Red)
2	K	I	LED_ Cathode (Black)

10. Block Diagram



11. Reliability

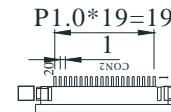
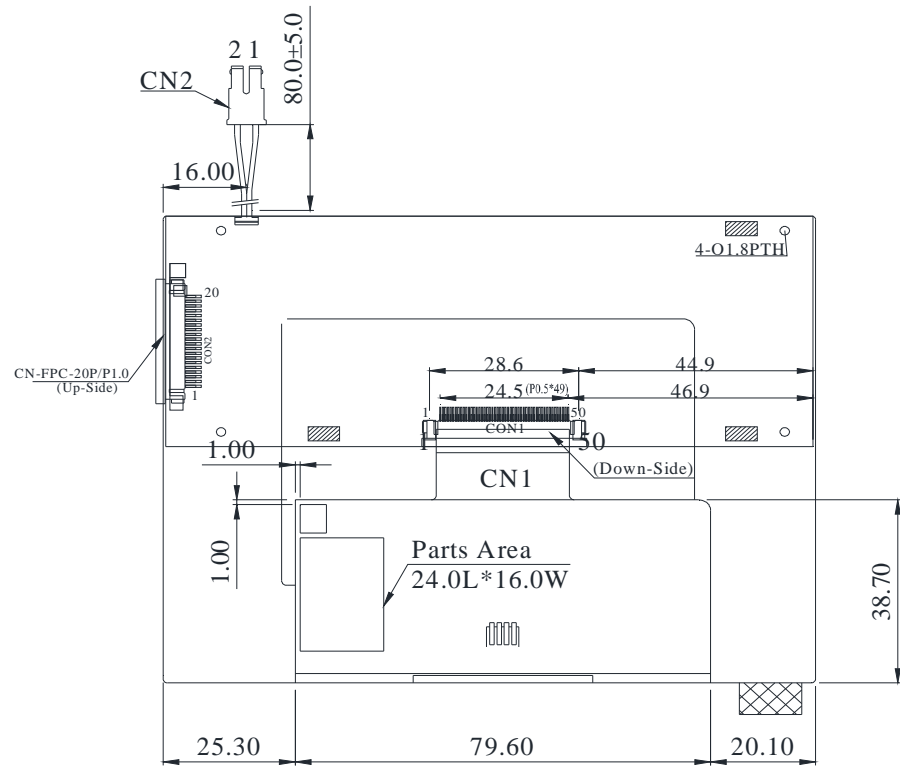
Content of Reliability Test (Wide temperature, -20°C~70°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 °C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation <div style="text-align: center;"> <p style="margin: 0;">-20°C 25°C 70°C</p> <p style="margin: 0;">30min ← 5min → 30min</p> <p style="margin: 0;">1 cycle</p> </div>	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 15mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V, RS=1.5kΩ CS=100pF 1 time	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

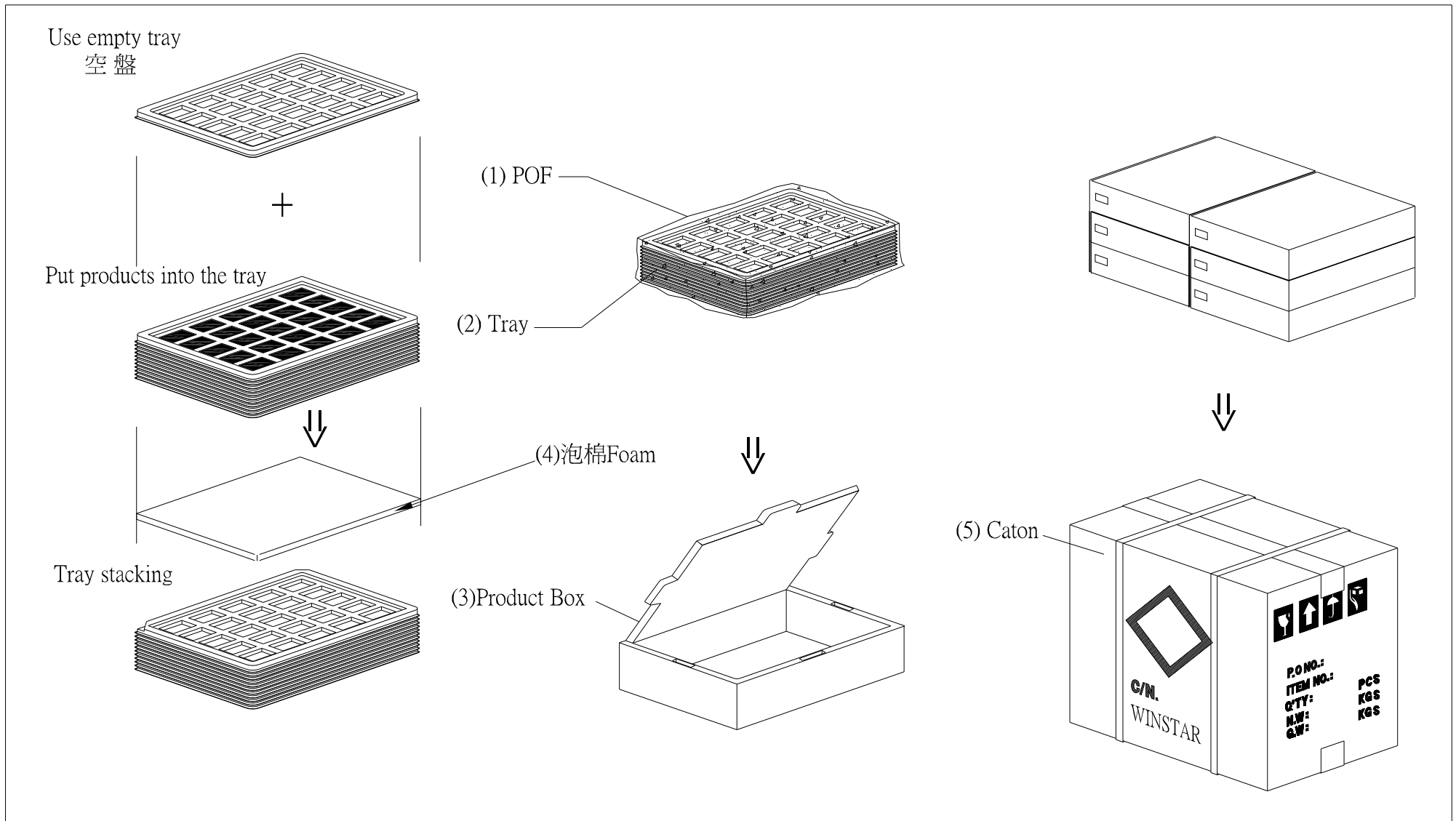


CON2

PIN NO.	SYMBOL
1	GND
2	VDD
3	NC
4	D/C
5	WR
6	RD
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	CS
16	UD
17	LR
18	RST
19	NC
20	NC

CN2

PIN NO.	SYMBOL
1	A
2	K



14.Initial Code For Reference

```
void Initial_SSD1963(void)
{
    Write_Command(0x01);
    Delay_ms(10);
    Write_Command(0xe0);
    Write_Parameter(0x01);
    Delay_ms(50);
    Write_Command(0xe0);
    Write_Parameter(0x03);
    Delay_ms(5);

    Write_Command(0xb0);
    Write_Data(0x0c);
    Write_Data(0x80);
    Write_Data(0x02);
    Write_Data(0x7F);
    Write_Data(0x01);
    Write_Data(0xDF);
    Write_Data(0x2d);

    Write_Command(0xf0);
    Write_Data(0x00);

    Write_Command(0xe6);
    Write_Data(0x02);
    Write_Data(0xff);
    Write_Data(0xff);

    ///////////////////////////////////////////////////
    Write_Command(0xb4);
    Write_Data(0x20);
    Write_Data(0xAF);
    Write_Data(0x00);
    Write_Data(0xA3);
    Write_Data(0x07);
    Write_Data(0x00);
    Write_Data(0x00);
    Write_Data(0x00);

    Write_Command(0xb6);
    Write_Data(0x01);
    Write_Data(0xEF);
    Write_Data(0x00);
    Write_Data(0x04);
    Write_Data(0x01);
    Write_Data(0x00);
    Write_Data(0x00);
```

```
Write_Command(0x2a);  
Write_Data(0x00);  
Write_Data(0x00);  
Write_Data(0x02);  
Write_Data(0x7f);
```

```
Write_Command(0x2b);  
Write_Data(0x00);  
Write_Data(0x00);  
Write_Data(0x01);  
Write_Data(0xdf);
```

```
Write_Command(0x29);
```

```
}
```



Module Number : _____

1、Panel Specification :

- 1. Panel Type : Pass NG , _____
- 2. View Direction : Pass NG , _____
- 3. Numbers of Dots : Pass NG , _____
- 4. View Area : Pass NG , _____
- 5. Active Area : Pass NG , _____
- 6. Operating Temperature : Pass NG , _____
- 7. Storage Temperature : Pass NG , _____
- 8. Others : _____

2、Mechanical Specification :

- 1. PCB Size : Pass NG , _____
- 2. Frame Size : Pass NG , _____
- 3. Material of Frame : Pass NG , _____
- 4. Connector Position : Pass NG , _____
- 5. Fix Hole Position : Pass NG , _____
- 6. Backlight Position : Pass NG , _____
- 7. Thickness of PCB : Pass NG , _____
- 8. Height of Frame to PCB : Pass NG , _____
- 9. Height of Module : Pass NG , _____
- 10. Others : Pass NG , _____

3、Relative Hole Size :

- 1. Pitch of Connector : Pass NG , _____
- 2. Hole size of Connector : Pass NG , _____
- 3. Mounting Hole size : Pass NG , _____
- 4. Mounting Hole Type : Pass NG , _____
- 5. Others : Pass NG , _____

4、Backlight Specification :

- 1. B/L Type : Pass NG , _____
- 2. B/L Color : Pass NG , _____
- 3. B/L Driving Voltage (Reference for LED Type) : Pass NG , _____
- 4. B/L Driving Current : Pass NG , _____
- 5. Brightness of B/L : Pass NG , _____
- 6. B/L Solder Method : Pass NG , _____
- 7. Others : Pass NG , _____

>> **Go to page 2** <<



Module Number : _____

5、Electronic Characteristics of Module :

- 1. Input Voltage : Pass NG , _____
- 2. Supply Current : Pass NG , _____
- 3. Driving Voltage for LCD : Pass NG , _____
- 4. Contrast for LCD : Pass NG , _____
- 5. B/L Driving Method : Pass NG , _____
- 6. Negative Voltage Output : Pass NG , _____
- 7. Interface Function : Pass NG , _____
- 8. LCD Uniformity : Pass NG , _____
- 9. ESD test : Pass NG , _____
- 10. Others : Pass NG , _____

6、Summary :

Sales signature : _____

Customer Signature : _____

Date : ____ / ____ / ____