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TFT Module Specification

MODEL: 13-101ZIEB0HD1-S

< ◇ >   PRELIMINARY SPECIFICATION

< ◆ >   APPROVAL SPECIFICATION

CUSTOMER
APPROVED BY
DATE:

DESIGNED	CHECKED	APPROVED
RD 2022.02.08	PM 2022.02.08	批准 2022.02.08 PM

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RECORD OF REVISION

Version	Revised Date	Page	Content
V1.0	2022/01/19	--	First specification
V1.1	2022/02/08	12	Update power on/off sequence.

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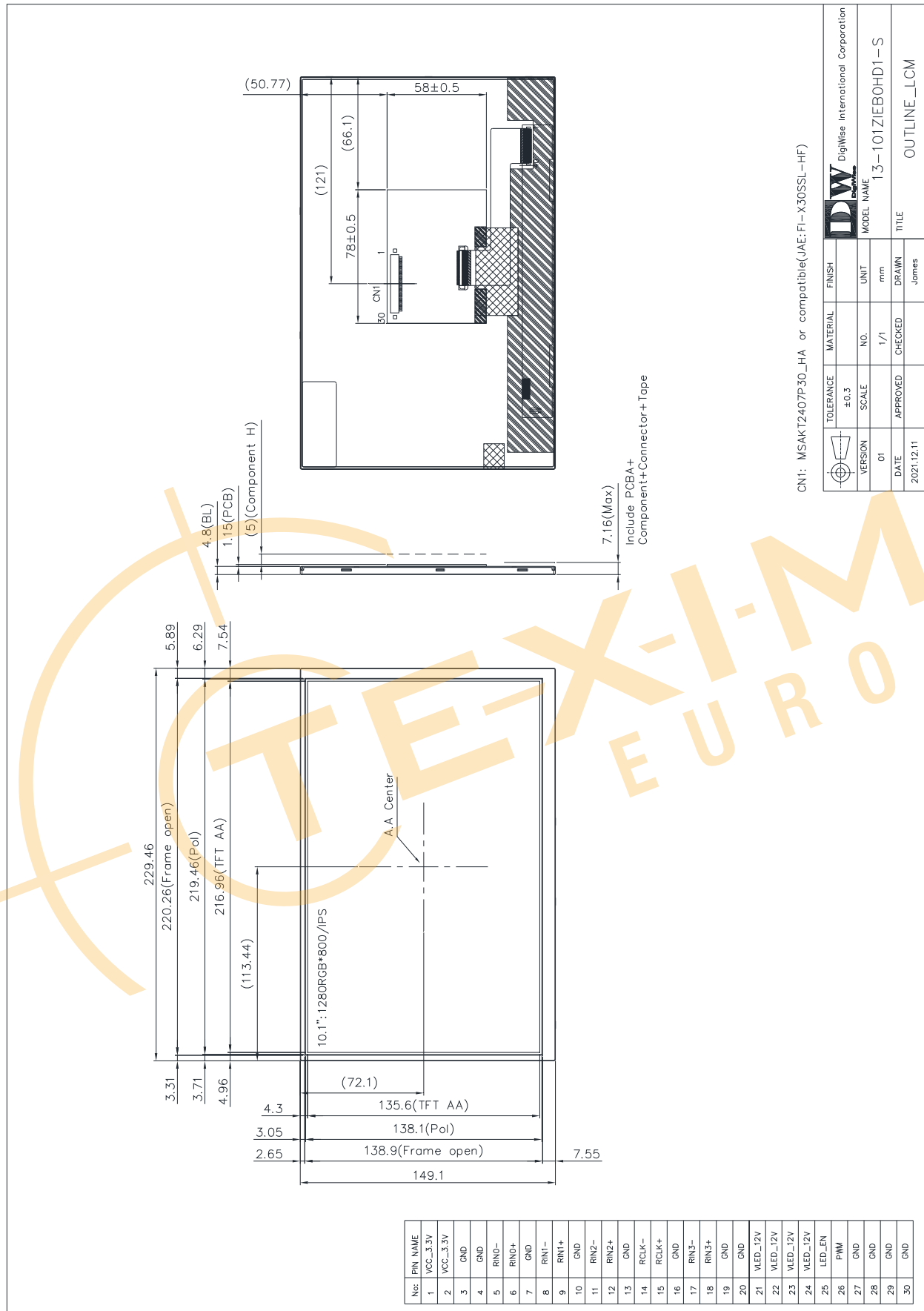E-mail: sales@digiwise.asia

1. GENERAL DESCRIPTION

1.1 Description

The specification is model 13-101ZIEB0HD1-S is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit, a backlight system. This TFT LCD has a 10.1 (16:10) inch diagonally measured active display area with WXGA (1280 horizontal by 800 vertical pixels) resolution.

1.2 Features:

No.	Item	Specification	Unit
1	Panel Size	10.1″	Inch
2	Number of Pixels	1280 (W) x RGB x 800 (H)	Pixels
3	Active Area	216.96 (W) × 135.6 (H)	mm
4	Pixel Pitch	0.1695 (W) x 0.1695 (H)	mm
5	Outline Dimension	229.46 (W) × 149.1 (H) × 4.8 (T)	mm
6	Number of Colors	16.7M	- -
7	Display Mode	IPS / Normally Black / Transmissive	- -
8	View Direction	Free direction	- -
9	Display Format	RGB vertical stripe	- -
10	Surface Treatment	HC	- -
11	Contrast Ratio	900 (Typ.)	- -
12	Luminance (cd/m^2)	1400 (Typ.)	cd/m2
13	Interface	LVDS 8 bit Interface	- -
14	Backlight	White LED	- -
15	Operation Temperature	-30 ~ 80	°C
16	Storage Temperature	-30 ~ 80	°C
17	Weight	(TBD)	g

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2. MECHANICAL SPECIFICATION

(50.77)

58±0.5

(66.1)

(121)

78±0.5

CN1

1

30

CN1: MSAKT2407P30_HA or compatible(JAE:FI-X30SSL-HF)

4.8(BL)
1.15(PCB)
(5)(Component H)

7.16(Max)

Include PCBA+
Component+Connector+Tape

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MODEL NAME: 13-101ZIEB0HD1-S

TITLE: OUTLINE_LCM

TOLERANCE	MATERIAL	FINISH		
±0.3				
SCALE	NO.	UNIT		
	1/1	mm		
VERSION	APPROVED	CHECKED	DRAWN	
01			James	
DATE				
2021.12.11				

5.89
6.29
7.54

229.46(Frame open)
220.26(Frame open)
219.46(Pol)
216.96(TFT AA)

(113.44)

A.A Center

10.1"; 1280RGB*800/IPS

3.31
3.71
4.96

4.3
(72.1)
135.6(TFT AA)

3.05
138.1(Pol)

2.65
138.9(Frame open)

7.55

149.1

No:	PIN NAME
1	VCC_3.3V
2	VCC_3.3V
3	GND
4	RIN0-
5	RIN0+
6	GND
7	RIN1-
8	RIN1+
9	GND
10	RIN2-
11	RIN2+
12	GND
13	RCLK-
14	RCLK+
15	GND
16	RIN3-
17	RIN3+
18	GND
19	GND
20	GND
21	VLED_12V
22	VLED_12V
23	VLED_12V
24	VLED_12V
25	LED_EN
26	PWM
27	GND
28	GND
29	GND
30	GND

3. PIN DESCRIPTION

3.1 TFT LCD Module (CN1)

Pin No.	Symbol	I/O	Function	Note
1	VCC	P	Power Supply Logic voltage +3.3V	
2	VCC	P	Power Supply Logic voltage +3.3V	
3	GND	P	Ground	
4	GND	P	Ground	
5	RIN0-	I	Negative LVDS differential data input	
6	RIN0+	I	Positive LVDS differential data input	
7	GND	P	Ground	
8	RIN1-	I	Negative LVDS differential data input	
9	RIN1+	I	Positive LVDS differential data input	
10	GND	P	Ground	
11	RIN2-	I	Negative LVDS differential data input	
12	RIN2+	I	Positive LVDS differential data input	
13	GND	P	Ground	
14	RCLK-	I	Negative LVDS differential clock input	
15	RCLK+	I	Positive LVDS differential clock input	
16	GND	P	Ground	
17	RIN3-	I	Negative LVDS differential data input	
18	RIN3+	I	Positive LVDS differential data input	
19	GND	P	Ground	
20	GND	P	Ground	
21	VLED_12V	P	Power Supply LED voltage +12V	
22	VLED_12V	P	Power Supply LED voltage +12V	
23	VLED_12V	P	Power Supply LED voltage +12V	
24	VLED_12V	P	Power Supply LED voltage +12V	
25	LED_EN	I	Back-light On/Off control	
26	PWM	I	Back-light Dimming control	
27	GND	P	Ground	
28	GND	P	Ground	
29	GND	P	Ground	
30	GND	P	Ground	

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4. ABSOLUTE MAXIMUM RATINGS

4.1 Electrical Absolute Rating

4.1.1 TFT LCD Module

Item	Symbol	Values		Unit	Note
		Min	Max.		
Power supply voltage	VCC	-0.3	3.6	V	
	VLED_12V	11	13	V	

4.1.2 Environment Absolute Rating

Item	Symbol	Values			Unit	Note
		Min	Typ	Max.		
Operating Temperature	Topa	-30		80	°C	Ambient
Storage Temperature	Tstg	-30		80	°C	temperature

5. BLOCK DIAGRAM

5.1 TFT LCD Module

```
┌─────────────────────────────────────┐  ┌──────────┐
│                                       │  │          │
│      ┌──────────────────────┐         │  │          │
│      │                      │         │  │  B       │
│      │   1280 x RGB x 800    │         │  │  a       │
│      │                      │         │  │  c       │
│      └──────────────────────┘         │  │  k       │
│                                       │  │  l       │
└─────────────────────────────────────┘  │  i       │
┌─────────────────────────────────────┐  │  g       │
│        Source IC+Gate IC              │  │  h       │
└─────────────────────────────────────┘  │  t       │
         ↑          ↑          ↑          │          │
┌─────────────────────────────────────┐  │  U       │
│          DC/DC Circuit          →     │  │  n       │
└─────────────────────────────────────┘  │  i       │
    ↑    ↑     ↑    ↑    ↑    ↑          │  t       │
                                          └──────────┘
```

VCC	RXIN0-		RXIN2-		CLK-	VLED_12V
GND	RXIN0+		RXIN2+		CLK+	GND
		RXIN1-		RXIN3-		PWM
		RXIN1+		RXIN3+		LED_EN
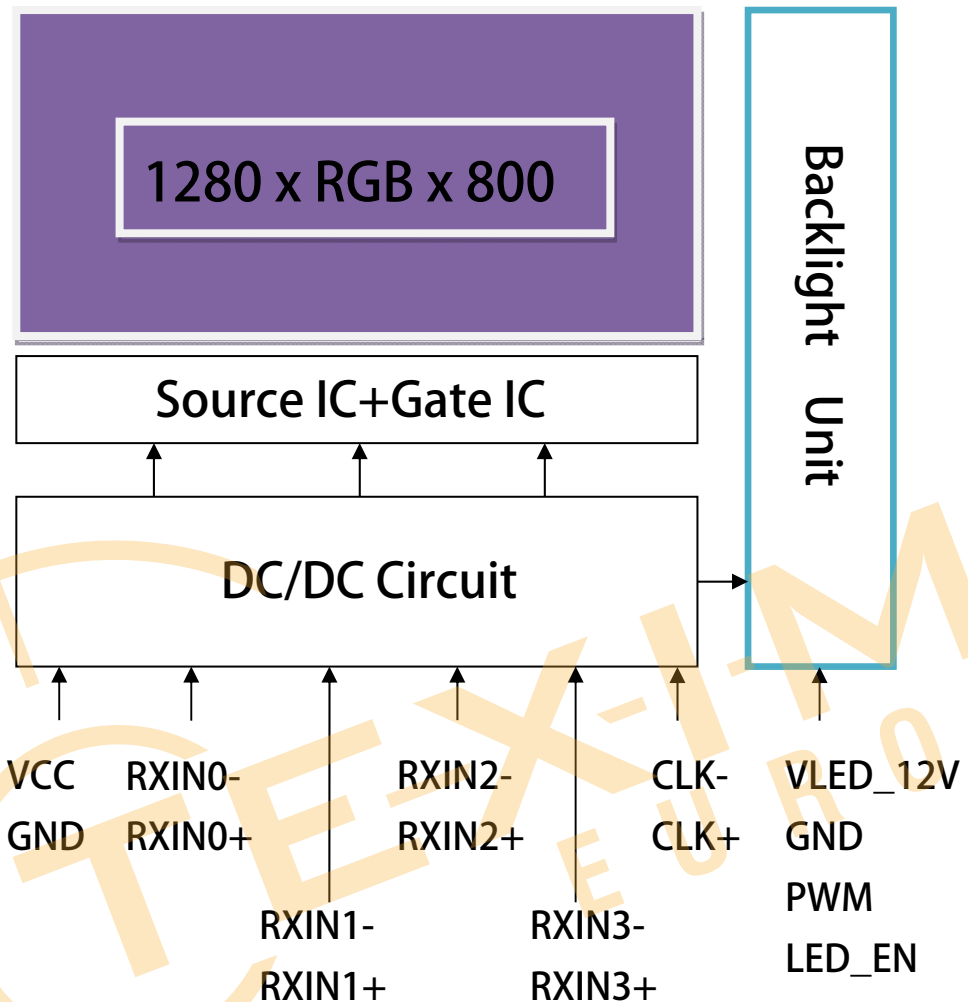
6. Relationship Between Displayed Color and Input

6.1 8 bit

CLKP									
CLKN									
D0P/N	R0	G0	R5	R4	R3	R2	R1	R0	G0
D1P/N	G1	B1	B0	G5	G4	G3	G2	G1	B1
D2P/N	B2	DE	VS	HS	B5	B4	B3	B2	DE
D3P/N	R6	-	B7	B6	G7	G6	R7	R6	-

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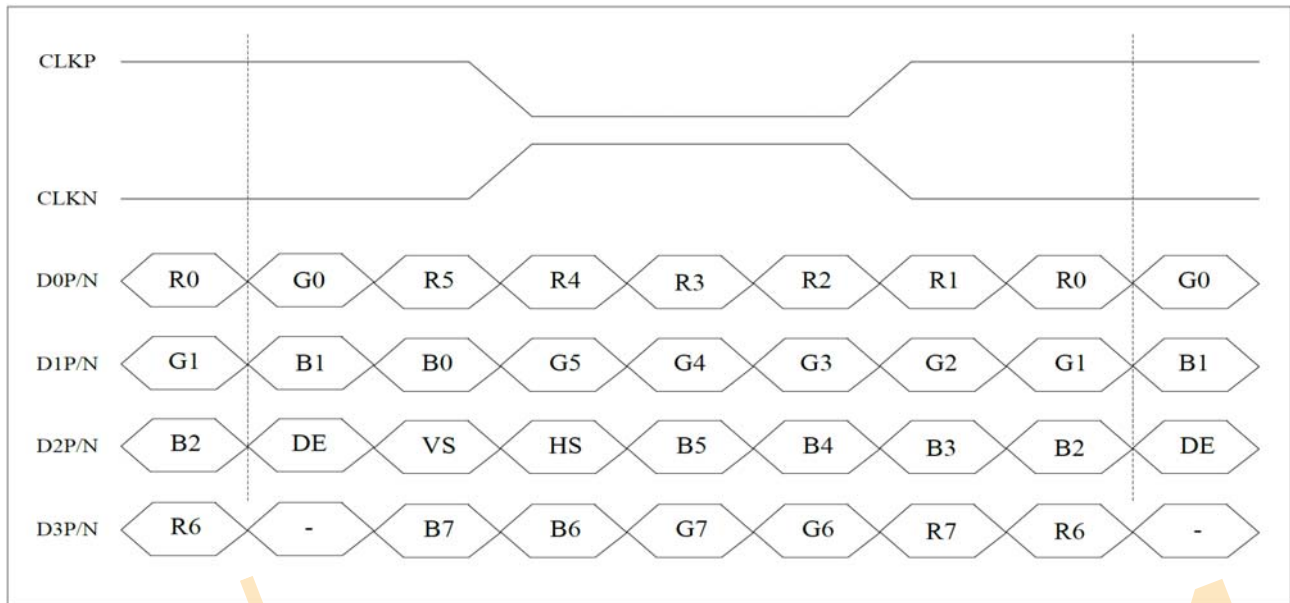
7. ELECTRICAL CHARACTERISTICS

7.1 TFT LCD Module

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Supply Voltage	VCC	3.0	3.3	3.6	V	
	VLED	11	12	13	V	
	VRF	-	-	100	mV p-p	Ripple voltage
Differential Input High Threshold	VTH	+100	+200	+300	mV	Vcm=+1.2V
Differential Input Low Threshold	VTL	-300	-200	-100	mV	Vcm=+1.2V
Magnitude differential Input Voltage	|VID|	200	-	600	mV	
Common Mode Voltage	Vcm	1	1.2	1.4	V	
Supply Current	ICC	-	270	300	mA	VCC=3.3V
	IDD	-	470	520	mA	VLED=12V
PWM frequency	PWM	100	-	8K	Hz	
PWM input voltage	VIH	1.6	-	-	V	
	VIL	-	-	0.8	V	
LED_EN input voltage	VIH	1.6	-	-	V	
	VIL	-	-	0.8	V	
LED life time		50000	-	-	Hr	(2)

Note 1:
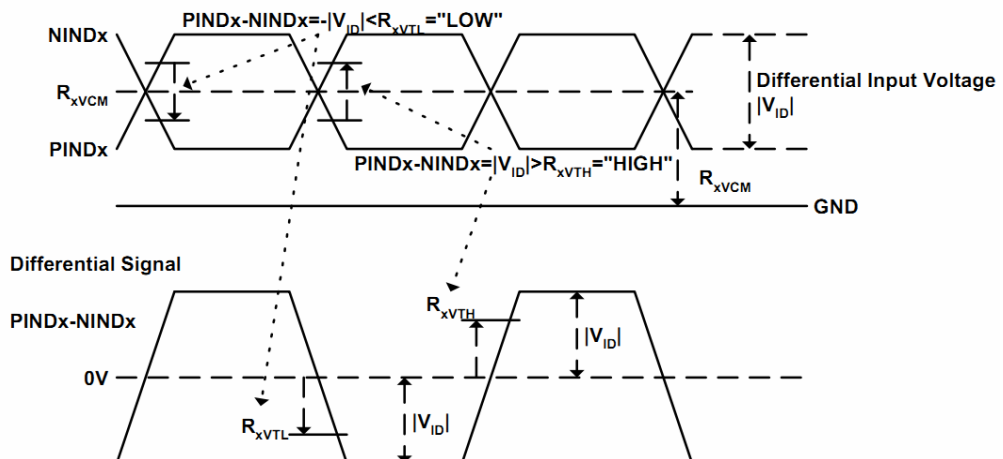
Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25℃ 60% RH.

7.2 INTERFACE SPECIFICATIONS

7.2.1 Timing

Signal	Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
DCLK	CLK frequency	$1/Tc$	66.3	72.4	78.9	MHz	
HSYNC	Horizontal Line	T_H	1380	1440	1500	Tc	
	HS Display Area	T_{HD}	-	1280	-	Tc	
	HS Blanking	$T_{HBP}+t_{HFP}$	100	160	220	Tc	
VSYNC	VS Period Time	T_V	824	838	872	T_H	
	VS Display Area	T_{VD}	-	800	-	T_H	
	VS Blanking	$T_{VBP}+T_{VFP}$	24	38	72	T_H	

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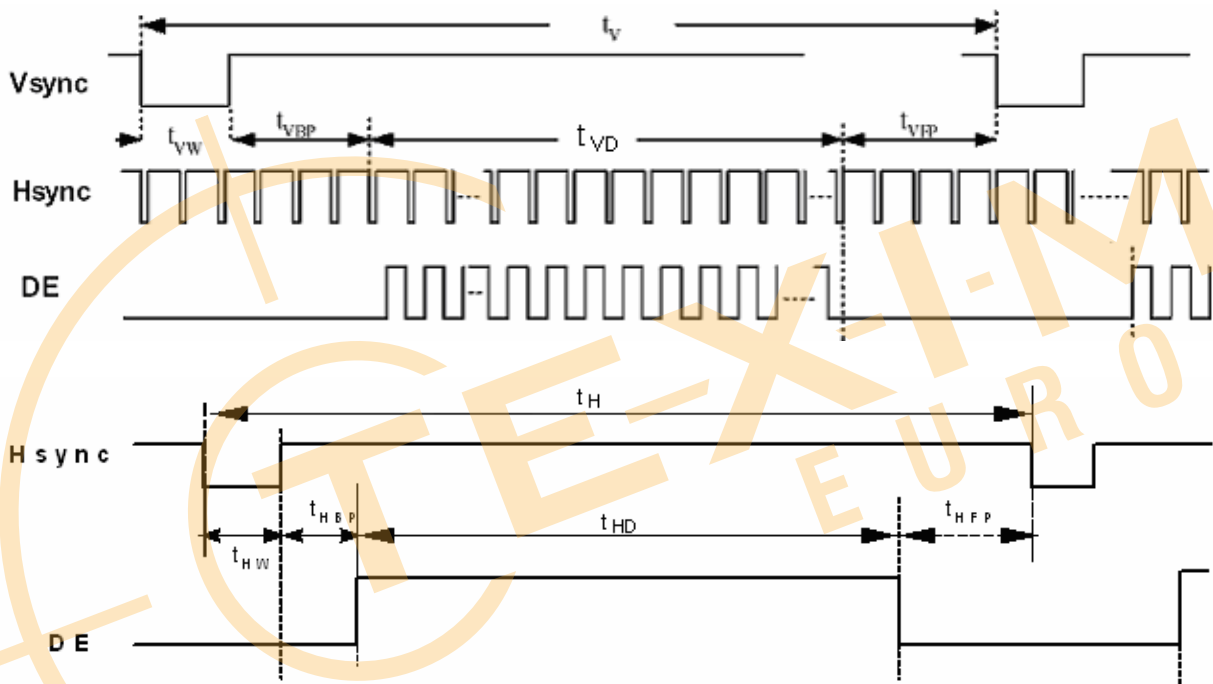
7.3 Power On / Off Sequence
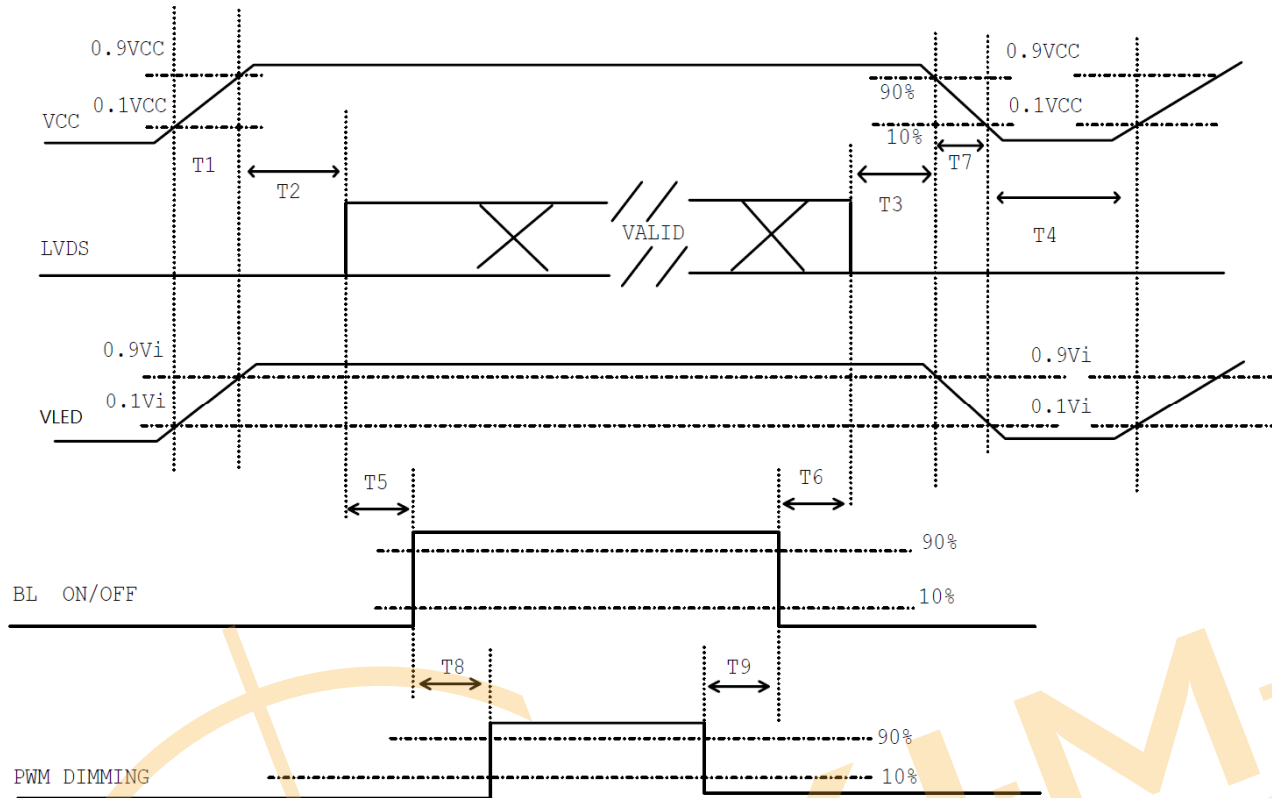
Parameter	Value			Units
	Min	Typ	Max	
T1	0.5	---	10	ms
T2	0	---	50	ms
T3	0	---	50	ms
T4	500	---	---	ms
T5	200	---	---	ms
T6	20	---	---	ms
T7	5	---	300	ms
T8	10	---	---	ms
T9	10	---	---	ms

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8. OPTICAL CHARACTERISTICS

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Brightness		--	Note1, Note 3, ($\theta = 0°$, Normal Viewing Angle)	1100	1400	--	cd/m2
Uniformity		B-uni		75	80	--	%
Contrast Ratio		CR		700	900	--	--
Response Time		Tr		--	10	20	ms
		Tf		--	15	30	ms
Color Chromaticity	White	Wx		0.275	0.315	0.355	--
		Wy		0.310	0.350	0.390	--
View angle	Horizontal	θ x+	Center CR≥10	70	80	--	
		θ x-		70	80	--	
	Vertical	θ Y+		70	80	--	
		θ Y-		70	80	--	
NTSC		--	--	--	73	--	%

Note：The following optical specifications shall be measured in a darkroom or equivalent state(ambient luminance ≤1 lux, and at room temperature). The operation temperature is 25°C±2°C. The measurement method is shown in Note1.

Note1: The method of optical measurement:
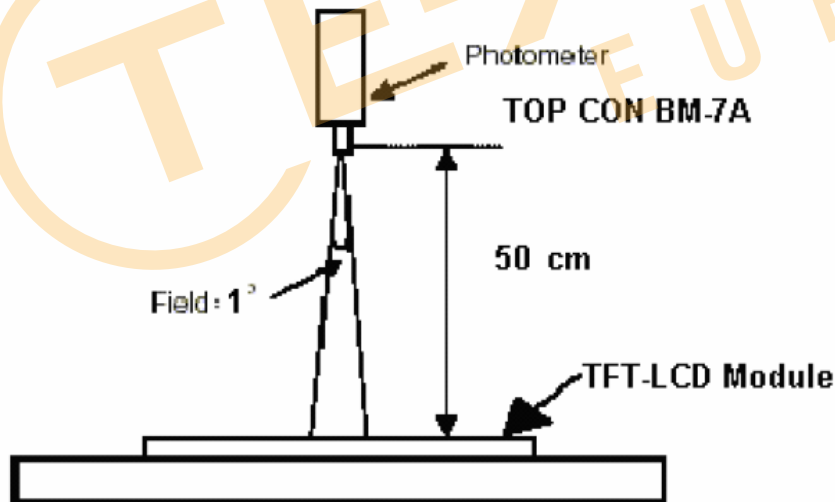
Note2: Measured at the center area of the panel and at the viewing angle of the θ x= θ y =0°

Note3: Definition of Contrast Ratio (CR):

CR = Luminance with all pixels in white state ÷ Luminance with all pixels in Black state

Note 4: Definition of Viewing Angle:
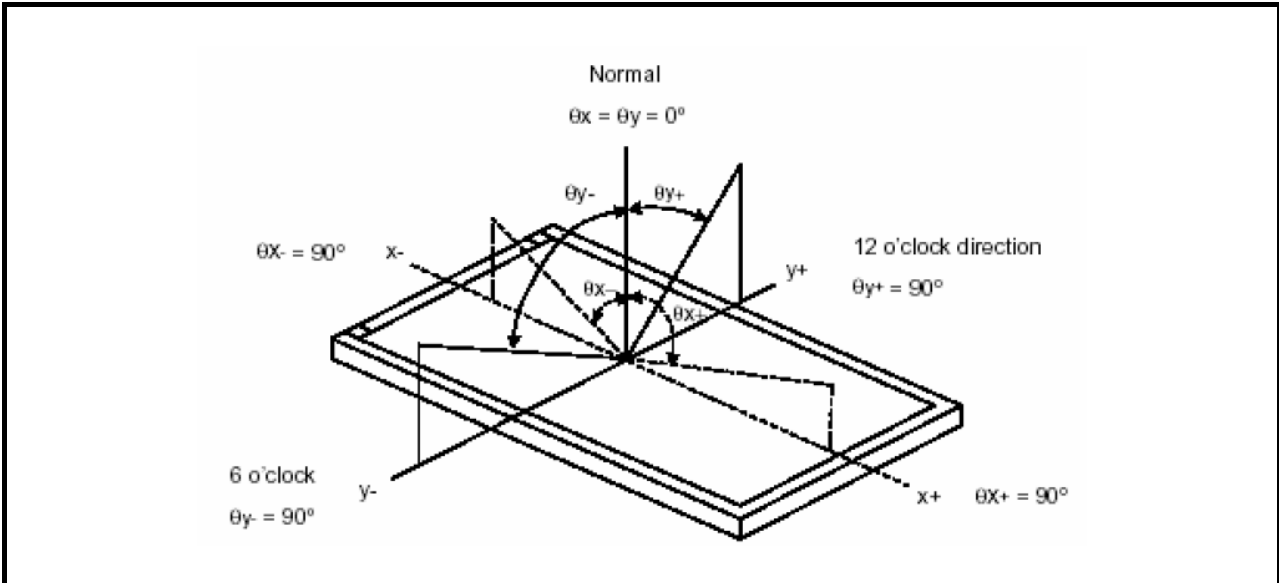
Note 5: Definition of Brightness Uniformity (B-uni):
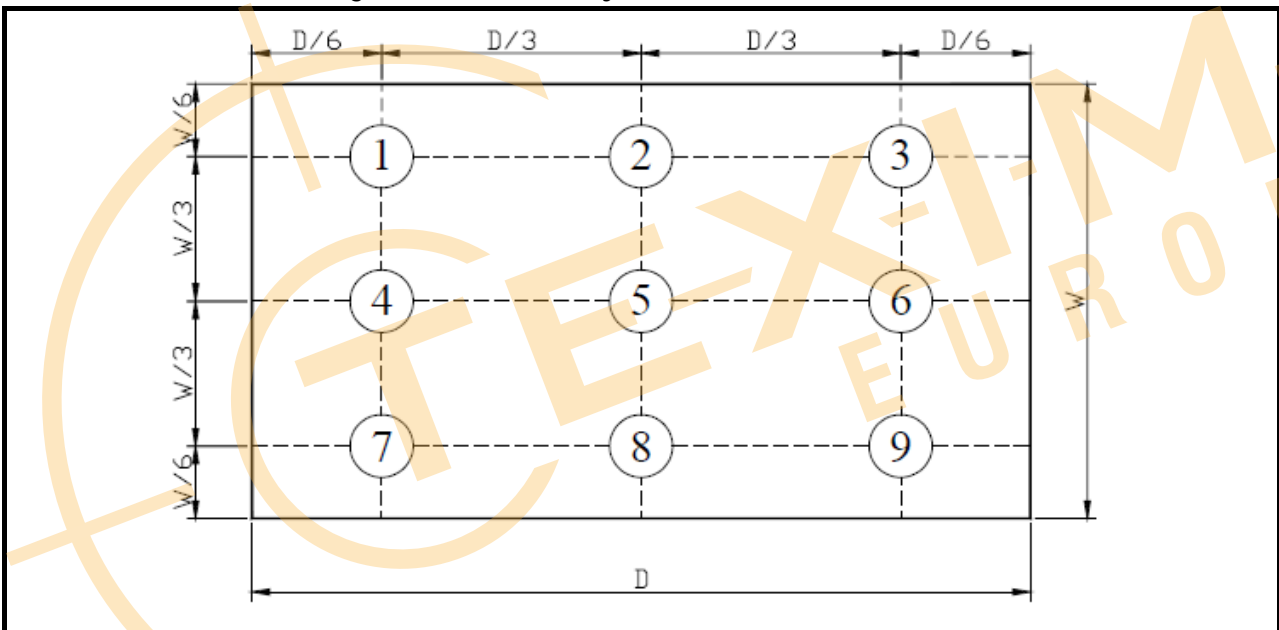
B-uni ＝(Minimum luminance of 9 points÷Maximum luminance of 9points)X100%

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Note 6: Definition of Response Time:

The Response Time is set initially by defining the "Rising Time (Tr)" and the "Falling Time (Tf)" respectively. Tr and Tf are defined as following figure
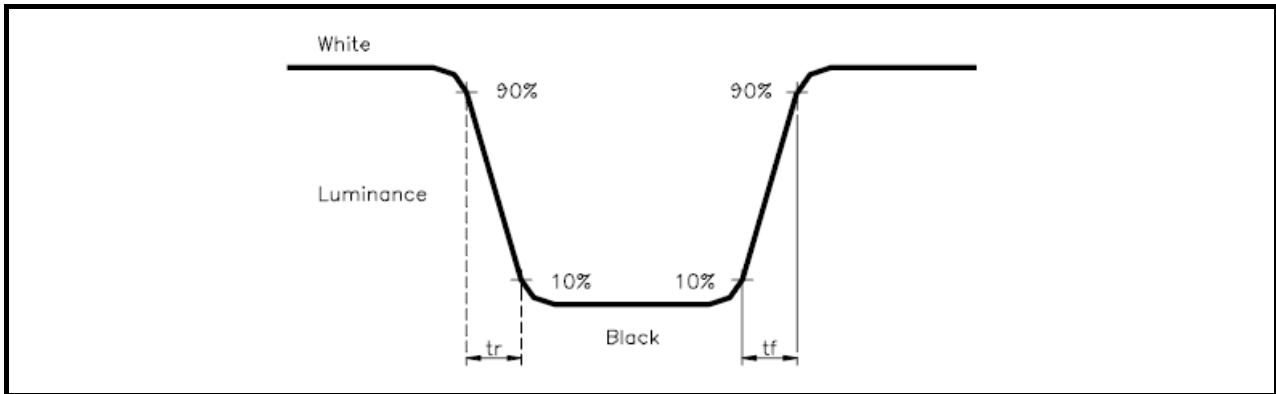
Note 7: Definition of Chromaticity:

The color coordinates (Wx,Wy),(Rx,Ry),(Gx,Gy),and (Bx,By) are obtained with all pixels in the viewing field at white, red, green, and blue states, respectively.

9. RELIABILITY

9.1 Test Condition

9.1.1 Temperature and Humidity(Ambient Temperature)
Temperature : 25 ± 5℃
Humidity : 65 ± 5%

9.1.2 Operation
Unless specified otherwise, test will be conducted under function state.

9.1.3 Container
Unless specified otherwise, vibration test will be conducted to the product itself without putting it in a container.

9.1.4 Test Frequency
In case of related to deterioration such as shock test. It will be conducted only once.

9.2 TESTS

No.	ITEM	CONDITION CRITERION
1	High Temperature Storage	80°C, 240 hrs
2	Low Temperature Storage	-30°C, 240 hrs
3	High Temperature Operating	80°C, 240 hrs
4	Low Temperature Operating	-30°C, 240 hrs
5	High Temperature/Humidity Non-Operating	60°C, 90%RH, 240 hrs
6	Temperature Shock Non-Operating	-30°C ←→ 80°C (0.5hr each), 100 cycles
7	Vibration Test Non-Operating	Frequency:0 ~ 55 Hz Amplitude:1.5 mm Sweep Time:11min Test Period:6 Cycles for each Direction of X,Y,Z
8	Electro-static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω

Note1: The test sample have recovery time for 24 hours at room temperature before the function check. In the standard conditions, there is no any touch panel function NG issue occurred.

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9.3 JUDGMENT STANDARD

The judgment of the above test should be made as follow:

Pass: Normal display image with no obvious non-uniformity and no line defect. Partial transformation of the module parts should be ignored.

Fail: No display image, obvious non-uniformity, or line defects.

9.4 INCOMING INSPECTION STANDARDS

No.	Parameter	Criteria
1	Operating	Display function: No Display malfunction (Major) Contrast ratio (Black, White): Does not meet specified range in the spec. (Major) (Note:3) Line Defect: No obvious Vertical and Horizontal line defect in bright, dark and colored. (Major) (Note:1) Point Defect : Active area ≤ 5 dots (Minor) (Note:1)
2	External Inspection (non-operating)	Dimension: Outline (Major) Bezel appearance: uneven (Minor) Scratch on the polarize: (Note:2)

Point Defect : Active area ≤ 5 dots (Minor) (Note:1)

Item	Acceptable number – Active Area	Total
Bright	2	5
Dark	4	

Non-uniformity: Visible through 5%ND filter. (Minor)

Foreign material in Black or White spots shape (W>1/4L)

Zone \ Dimension	Acceptable number	Class Of Defects	AQL Level
D> 0.5	0	Minor	1.5
0.3 < D ≤ 0.5	5		
D ≤ 0.3	*		

D = (Long + Short) / 2   * : Disregard

Foreign Material in Line or spiral shape (W≤1/4L) (Note: 4)

L (mm)	W(mm)	Acceptable number	Class Of Defects	AQL Level
L >5	W>0.1	0	Minor	1.5
0.5 < L ≤ 5	0.03 < W≤0.1	5		
L ≤0.5	W≤0.03	*		

L : Length   W : Width   * : Disregard

Scratch on the polarize: (Note:2)

L (mm)	W(mm)	Acceptable number	Class Of Defects	AQL Level
--	W>0.1	0	Minor	1.5
L ≤ 3	W≤0.1	3		

L : Length   W : Width   * : Disregard

Dent or bubble on the polarize (Note:2)

Zone \ Dimension	Acceptable number	Class Of Defects	AQL Level
D≤0.3	*	Minor	1.5
D≤0.5	3		

D = (Long + Short) / 2   * : Disregard

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| Mura/Waving/Hot spot | Not visible through 5% ND filter in 50% gray or judge by limit sample if necessary |
| COG Mura | Not visible through 1% ND filter in 50% gray or judge by limit sample if necessary |

			Definition
Class of defects	**Major**	AQL 0.65%	It is a defect that is likely to result in failure or to reduce materially the usability of the product for the intended function.
	Minor	AQL 1.5%	It is a defect that will not result in functioning problem with deviation classified.

Note1:

(a)Bright point defect is defined as point defect of R,G,B with area >1/2 pixel respectively

(b)Dark point defect is defined as visible in full white pattern.

(c)Definition of distribution of point defect is as follows:

   -minimum separation between dark point defects should be larger than 5mm.

   -minimum separation between bright point defects should be larger than 5mm.

(d)Definition of joined bright point defect and joined dark point defect are as follows:

   -Two or more joined bright point defects must be nil.

   -Three joined dark point defects must be nil.

   -Coupling of one dark and one bright point in junction is counted as one dark and bright
    spot with 1 pair maximum.

   -Two Joined dark point is counted as two dark points with 2 pair maximum.

Note2: The external inspection should be conducted at the distance 30± 5cm between the eyes of inspector and the panel.

Note3: Luminance measurement for contrast ratio is at the distance 50± 5cm between the detective head and the panel with ambient luminance less than 1 lux. Contrast ratio is obtained at optimum view angle.

Note4: W-Width in mm , L-length of Max.(L1,L2) in mm.

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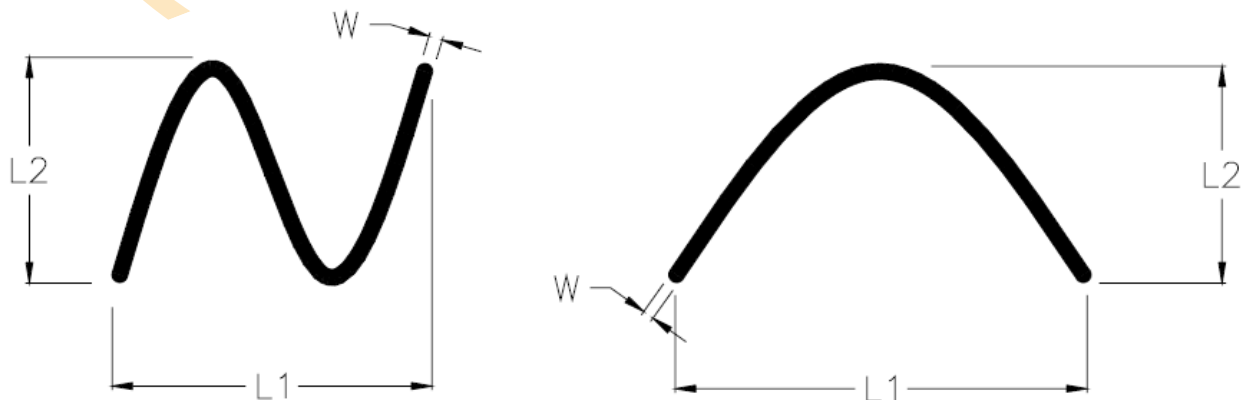
9.5 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer.

Lot size: Quantity of shipment lot per model.

Sampling type: normal inspection, single sampling
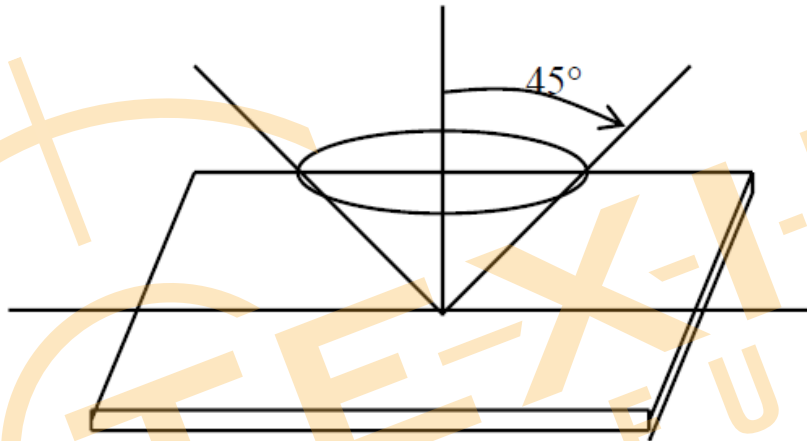
Sampling table: MIL-STD-105E

Inspection level: Level II

9.6 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.

$\theta \leq 45°$ inspection under non-operating condition.

$\theta \leq 5°$ inspection under operating condition

10. PRECAUTION RELATING PRODUCT HANDLING

10.1 SAFETY

10.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.

10.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

10.2 HANDLING

10.2.1 Avoid any strong mechanical shock which can break the glass.

10.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.

10.2.3 Do not remove the panel or frame from the module.

10.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully, Do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)

10.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.

10.2.6 Do not touch the display area with bare hands , this will stain the display area.

10.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

10.2.8 To control temperature and time of soldering is 280 ± 10°C and 3-5 sec.

10.2.9 To avoid liquid (include organic solvent) stained on LCM.

10.3 STORAGE

10.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.

10.3.2 Do not place the module near organics solvents or corrosive gases.

10.3.3 Do not crush, shake, or jolt the module.