

Distributed by:



Doc. Number :

- Tentative Specification
- Preliminary Specification
- Approval Specification

MODEL NO.: G121XCE

SUFFIX: L01

Customer:

APPROVED BY

SIGNATURE

Name / Title

Note

Please return 1 copy for your confirmation with your signature and comments.

Approved By	Checked By	Prepared By
陳立錚	林秋森	許文進

- CONTENTS -

1. GENERAL DESCRIPTION.....	5
1.1 OVERVIEW.....	5
1.2 FEATURES.....	5
1.3 APPLICATION.....	5
1.4 GENERAL SPECIFICATIONS.....	5
1.5 MECHANICAL SPECIFICATIONS.....	6
2. ABSOLUTE MAXIMUM RATINGS.....	7
2.1 ABSOLUTE RATINGS OF ENVIRONMENT.....	7
2.2 ELECTRICAL ABSOLUTE RATINGS.....	7
2.2.1 TFT LCD MODULE.....	7
2.2.2 BACKLIGHT UNIT.....	7
3. ELECTRICAL CHARACTERISTICS.....	8
3.1 TFT LCD MODULE.....	8
3.2 BACKLIGHT UNIT.....	9
4. BLOCK DIAGRAM.....	10
4.1 TFT LCD MODULE.....	10
5. INPUT TERMINAL PIN ASSIGNMENT.....	11
5.1 TFT LCD MODULE.....	11
5.2 BACKLIGHT UNIT(CONVERTER CONNECTOR PIN).....	12
5.3 COLOR DATA INPUT ASSIGNMENT.....	13
6. INTERFACE TIMING.....	15
6.1 INPUT SIGNAL TIMING SPECIFICATIONS.....	15
6.2 POWER ON/OFF SEQUENCE.....	17
6.3 THE INPUT DATA FORMAT.....	19
6.4 SCANNING DIRECTION.....	20
7. OPTICAL CHARACTERISTICS.....	21
7.1 TEST CONDITIONS.....	21
7.2 OPTICAL SPECIFICATIONS.....	21
8. RELIABILITY TEST CRITERIA.....	24
9. PACKAGING.....	25
9.1 PACKING SPECIFICATIONS.....	25
9.2 PACKING METHOD.....	25
9.3 UN-PACKING METHOD.....	26
10. DEFINITION OF LABELS.....	27
10.1 MODULE LABEL.....	27
11. PRECAUTIONS.....	28

11.1 ASSEMBLY AND HANDLING PRECAUTIONS.....	28
11.2 STORAGE PRECAUTIONS	28
11.3 OTHER PRECAUTIONS.....	29
12. MECHANICAL CHARACTERISTICS.....	30



REVISION HISTORY

Version	Date	Section	Description
2.0	,2017.12	All	G121XCE-L01 Approval Spec. was first issued.
2.1	2019.03	3.2 5	To remove note for power on/off sequence. 5. INPUT TERMINAL PIN ASSIGNMENT To remove NC selection for PIN 4th 、 17th 、 18th . To remove note(3) for NC selection. To add note(4) description about SELLVDS notice.
2.2	2019.04	10	10.1 MODULE LABEL To modify content of label (remove Rev 、 add Factory ID)
2.3	2020.05	5	5. INPUT TERMINAL PIN ASSIGNMENT To change the definition of 11th & 14th PIN (Ground→NC) 5.2 BACKLIGHT UNIT(Converter connector pin) To add the compatible part no. of converter connector.(P-Two)
2.4	2020.07	5	5.2 BACKLIGHT UNIT(Converter connector pin) To remove the compatible part no. of converter connector.(P-Two)
2.5	2021.05	5 8 9 11 15-17 20-23 32-38	1.4 GENERAL SPECIFICATION To modify power consumption to be 12.52W 3.1 TFT LCD MODULE To modify max/min of Vcc 、 Icc 、 power consumption 、 format of the table. 3.2 BACKLIGHT UNIT To revise the PWM Control Duty Ratio and the definition of note(2) 、 (3). 5.1 TFT LCD MODULE To add the 2nd source of LVDS CNT(P-Two) in Note(1) . 6.1 INPUT SIGNAL TIMING SPECIFICATIONS To revise the table 、 definition and notes in this chapter. Including adding some diagram in note. 7.OPTICAL CHARACTERISTICS To revise the format 、 definition and notes in this chapter. Add Appendix . SYSTEM COVER DESIGN NOTICE
2.6	2021.05	23 39	7.2 OPTICAL SPECIFICATIONS To add some definition of module placement in Note(5). Appendix . SYSTEM COVER DESIGN NOTICE To add some notice for Lamination.

1. GENERAL DESCRIPTION

1.1 OVERVIEW

The G121XCE-L01 model is a 12.1" TFT-LCD IAV module with a white LED Backlight Unit and a 20-pin 1ch-LVDS interface. This module supports 1024 x 768 XGA mode and displays 262k/16.7M colors. The converter for the Backlight Unit is built in.

1.2 FEATURES

- Wide viewing angle
- High contrast ratio
- XGA (1024 x 768 pixels) resolution
- Wide operating temperature
- DE (Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface
- Reversible-scan direction
- RoHS Compliance

1.3 APPLICATION

- TFT LCD Monitor
- Industrial Application
- Amusement
- Vehicle

1.4 GENERAL SPECIFICATIONS

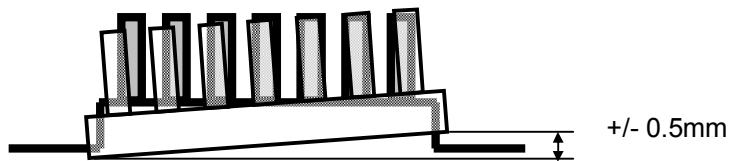
Item	Specification	Unit	Note
Diagonal Size	12.1	inch	(1)
Active Area	245.76(H) x 184.32(V)	mm	
Bezel Opening Area	249.0 x 187.5	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1024 x R.G.B. x 768	pixel	-
Pixel Pitch	0.240(H) x 0.240(V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262k/16.7M	color	-
Display Mode	Normally black	-	-
Surface Treatment	Hard coating (3H), Anti-Glare	-	-
Module Power Consumption	12.52W (white pattern)	W	Typ. (3)

1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	260	260.5	261	mm	(1)
	Vertical (V)	203.5	204	204.5	mm	
	Depth (D)	7.9	8.4	8.9	mm	
Weight			490	510	g	-
I/F connector mounting position		The mounting inclination of the connector makes the screen center within $\pm 0.5\text{mm}$ as the horizontal.			-	(2)

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

(2) Connector mounting position



(3) The Module Power Consumption is specified at 3.3V, white pattern and 100% duty for LED backlight.



3. ELECTRICAL CHARACTERISTICS

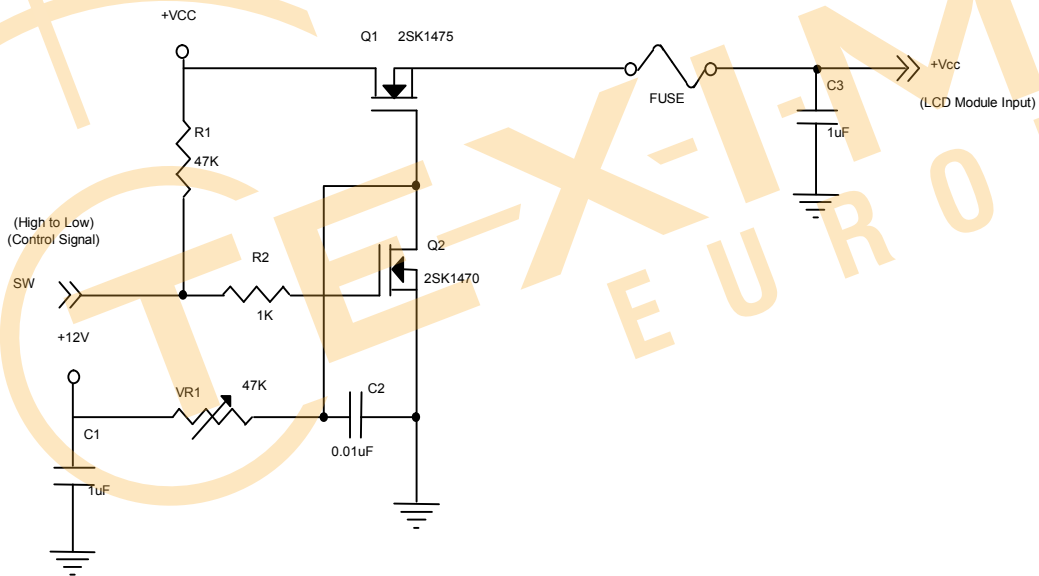
3.1 TFT LCD MODULE

Ta = 25 ± 2 °C

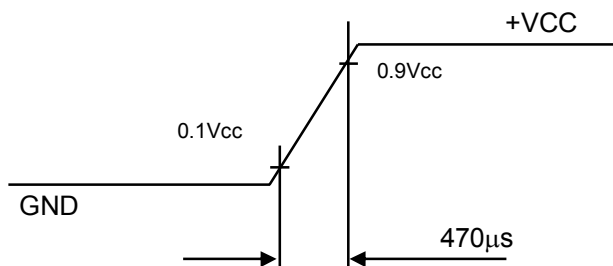
Parameter	Symbol	Value			Unit	Note	
		Min.	Typ.	Max.			
Power Supply Voltage	V _{CC}	3.15	3.3	3.45	V		
Ripple Voltage	V _{RP}	-	-	200	mVp-p		
Rush Current	I _{RUSH}	-	-	4	A	(2)	
Power Supply Current	White	-	520	620	mA	(3)a	
	Black	-	420	510	mA	(3)b	
LVDS differential input voltage	V _{id}	100	-	600	mV		
LVDS common input voltage	V _{ic}	1.0	1.2	1,4	V		
Power Consumption	P _L	-	1.72	2.05	W		
Differential Input Voltage for LVDS Receiver Threshold	"H" Level	V _{IH}	+100	-	-	mV	
	"L" Level	V _{IL}	-	-	-100	mV	
Terminating Resistor	R _T	-	100	-	Ohm		

Note (1) The assembly should be always operated within above ranges.

Note (2) Measurement Conditions:



Vcc rising time is 470µs

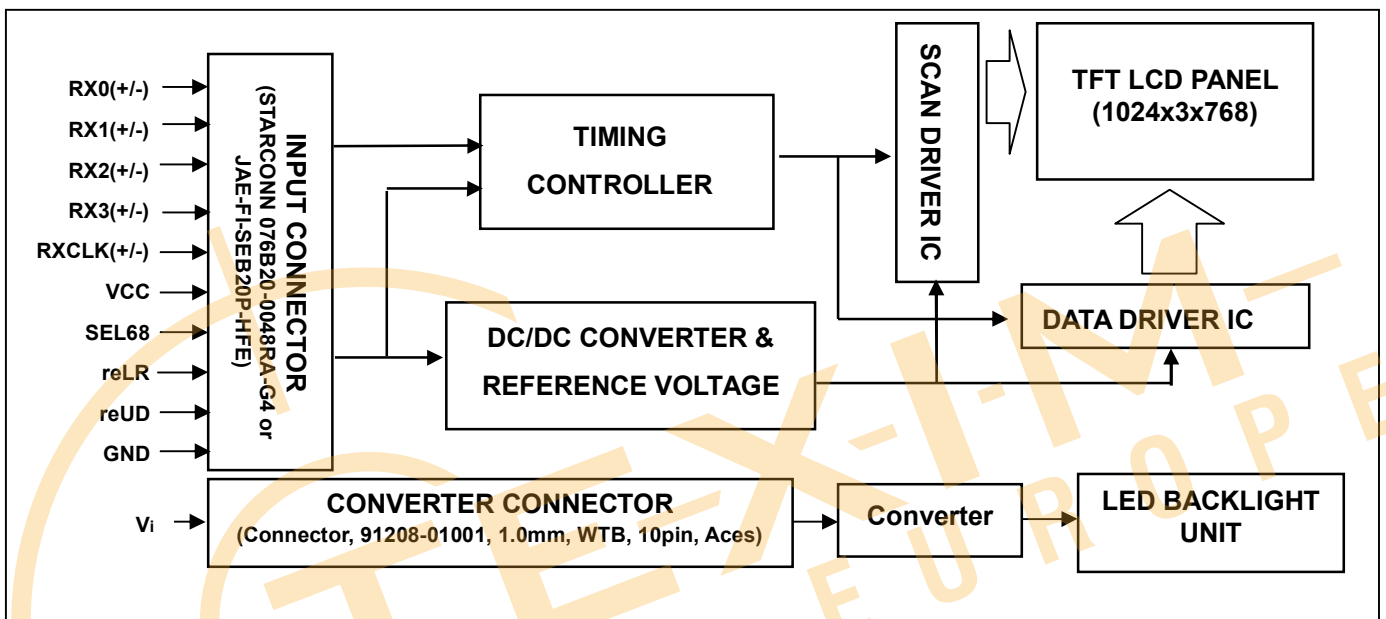


Note (2) At 190 ~1kHz PWM control frequency, duty ratio range is restricted from 5% to 100%. 1K ~20kHz PWM control frequency, duty ratio range is restricted from 20% to 100%. If PWM control frequency is applied in the range from 1KHz to 20KHZ, The “non-linear” phenomenon the Backlight Unit may be found. So It’s a suggestion that PWM control frequency should be less than 1KHz.

Note (3) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at $T_a = 25 \pm 2 \text{ }^\circ\text{C}$ and Duty 100% until the brightness becomes $\leq 50\%$ of its original value. Operating LED at high temperature condition will reduce life time and lead to color shift.

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

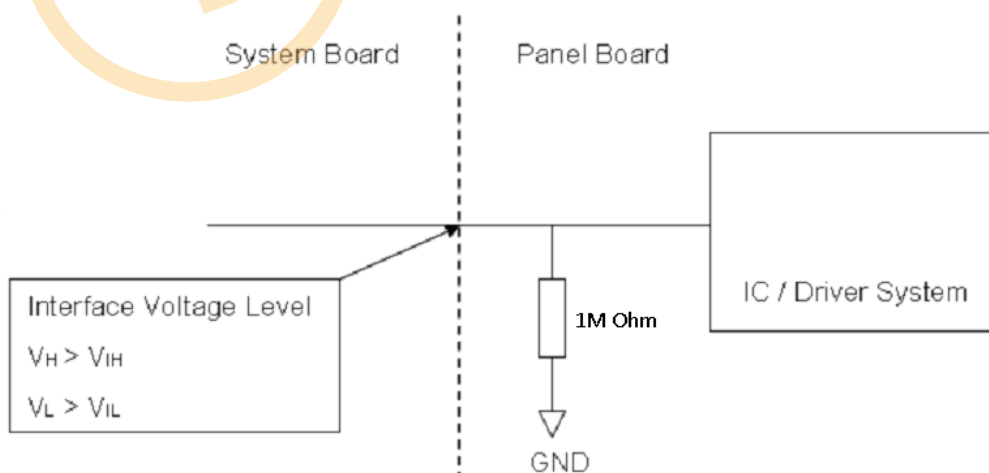
Pin	Name	Description	Remark
1	RX3+	Differential Data Input, CH3 (Positive)	
2	RX3-	Differential Data Input, CH3 (Negative)	
3	NC	NC	
4	SEL68	LVDS 6/8 bit select function control, Low → 6 bit Input Mode High → 8bit Input Mode	Note (3) (4)
5	GND	Ground	
6	RXC+	Differential Clock Input (Positive)	
7	RXC-	Differential Clock Input (Negative)	
8	GND	Ground	
9	RX2+	Differential Data Input , CH2 (Positive)	
10	RX2-	Differential Data Input , CH2 (Negative)	
11	NC	For LCD internal use only, Do not connect	
12	RX1+	Differential Data Input , CH1 (Positive)	
13	RX1-	Differential Data Input, CH1 (Negative)	
14	NC	For LCD internal use only, Do not connect	
15	RX0+	Differential Data Input, CH0 (Positive)	
16	RX0-	Differential Data Input, CH0 (Negative)	
17	reLR	Horizontal Reverse Scan Control, Low → Normal Mode. High → Horizontal Reverse Scan	Note (3) (4)
18	reUD	Vertical Reverse Scan Control, Low → Normal Mode, High → Vertical Reverse Scan	Note (3) (4)
19	VCC	Power supply	
20	VCC	Power supply	

Note (1) Connector Part No.: P-Two 187191-20101-3 or STARCONN 076B20-0048RA-G4 or equivalent.

Note (2) User's connector Part No.: JAE FI-SE20ME or equivalent.

Note (3) "Low" stands for 0V. "High" stands for 3.3V.

Note (4) SEL68, reLR, reUD



5.2 BACKLIGHT UNIT(CONVERTER CONNECTOR PIN)

Pin	Symbol	Description	Remark
1	V_i	Converter input voltage	12V
2	V_i	Converter input voltage	12V
3	V_i	Converter input voltage	12V
4	V_i	Converter input voltage	12V
5	V_{GND}	Converter ground	Ground
6	V_{GND}	Converter ground	Ground
7	V_{GND}	Converter ground	Ground
8	V_{GND}	Converter ground	Ground
9	EN	Enable pin	3.3V, Note (3)
10	ADJ	Backlight Adjust	PWM Dimming (190-210Hz, Hi: 3.3V _{DC} , Lo: 0V _{DC}), Note (3)

Note (1) Connector Part No.: 91208-01001-H01 (ACES) or equivalent.

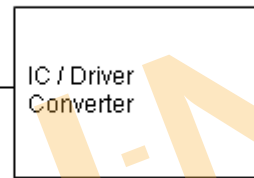
Note (2) User's connector Part No.: 91209-01011 (ACES) or equivalent--

Note (3) EN(BLON), ADJ(E_PWM) as shown below :

BLON Pin

System Board Panel Board

EN Pin



E_PWM Pin

System Board Panel Board

Dimming Pin



5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	0	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Gray Scale Of Blue	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	0
Blue(63)	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	

Note (1) 0: Low Level Voltage, 1: High Level Voltage

PRODUCT SPECIFICATION

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																						
		Red								Green								Blue						
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮		
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮		
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮		
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮		
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮		
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮		
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

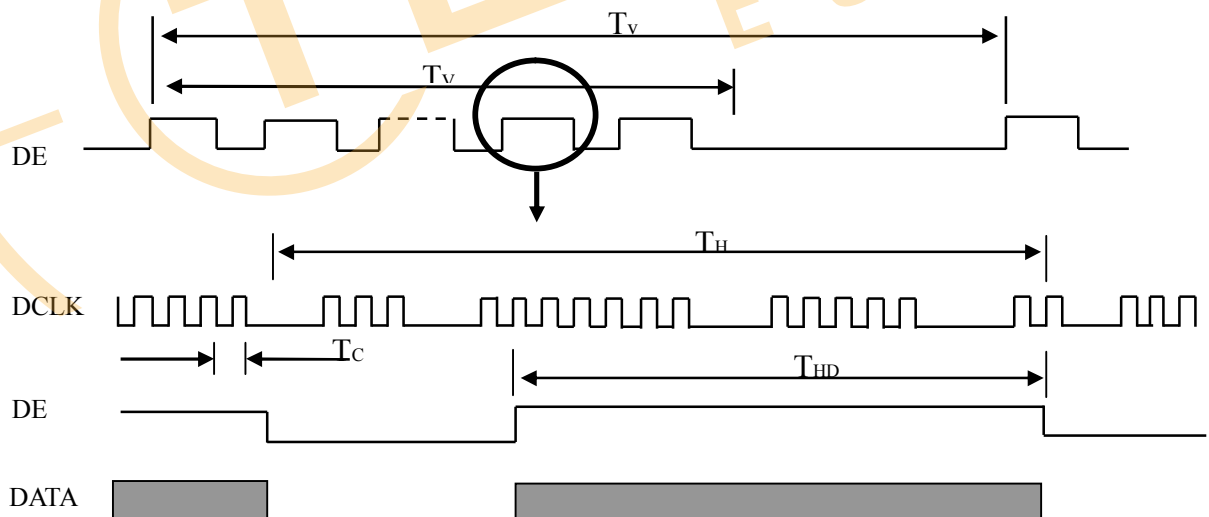
The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	F_c	57.7	65	73.6	MHz	-
	Period	T_c	13.6	15.4	17.3	ns	
	Input cycle to cycle jitter	T_{rcj}	---	---	200	ns	(a)
	Input Clock to data skew	TLVCCS	$-0.02 \cdot T_c$	---	$0.02 \cdot T_c$	ps	(b)
	Spread spectrum modulation range	$F_{clk_{in_mod}}$	$0.987 \cdot F_c$	---	$1.013 \cdot F_c$	MHz	(c)
	Spread spectrum modulation frequency	F_{SSM}	---	---	200	KHz	
	High Time	T_{ch}	---	4/7	---	T_{ch}	
	Low Time	T_{cl}	---	3/7	---	T_{ch}	
Vertical Display Term	Frame Rate	F_r	---	60	---	Hz	$T_v = T_{vd} + T_{vb}$
	Total	T_v	776	806	838	Th	-
	Active Display	T_{vd}	768	768	768	Th	-
	Blank	T_{vb}	8	38	70	Th	-
Horizontal Display Term	Total	T_h	1240	1344	1464	T_c	$T_h = T_{hd} + T_{hb}$
	Active Display	T_{hd}	1024	1024	1024	T_c	-
	Blank	T_{hb}	216	320	440	T_c	-

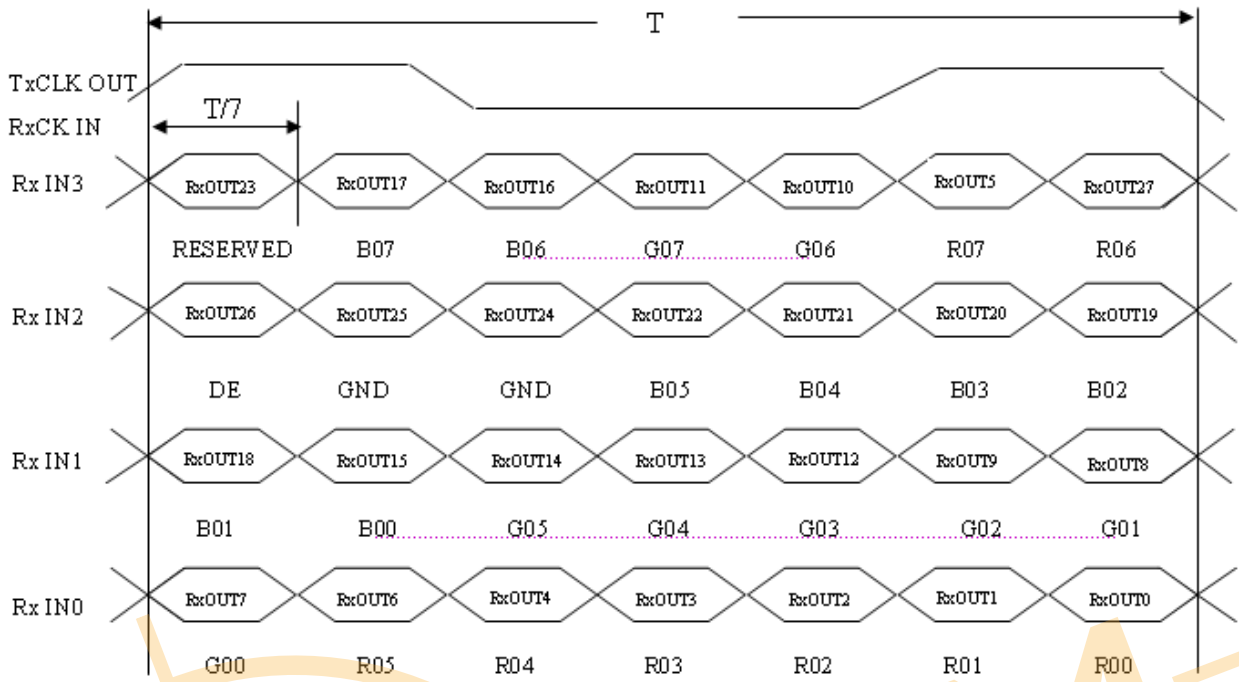
Note (1) Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

Note (2) The $T_v(T_{vd} + T_{vb})$ must be integer, otherwise, the module would operate abnormally.

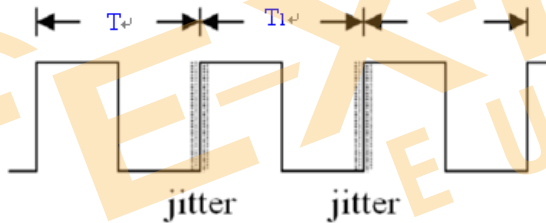
INPUT SIGNAL TIMING DIAGRAM



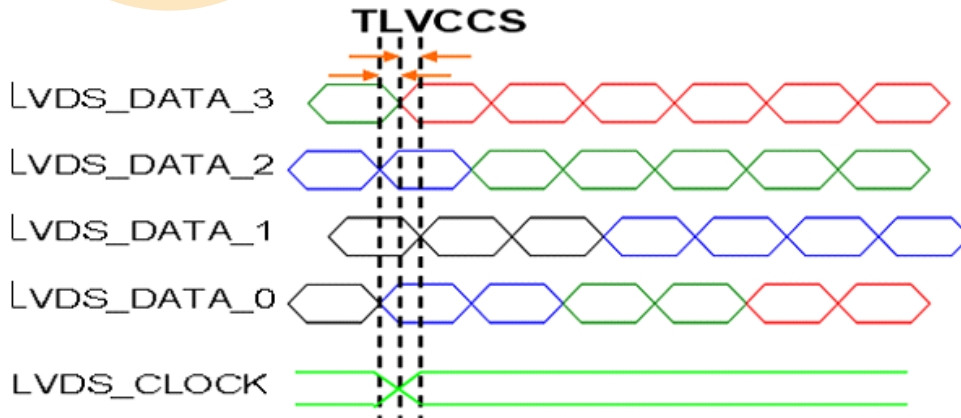
TIMING DIAGRAM of LVDS



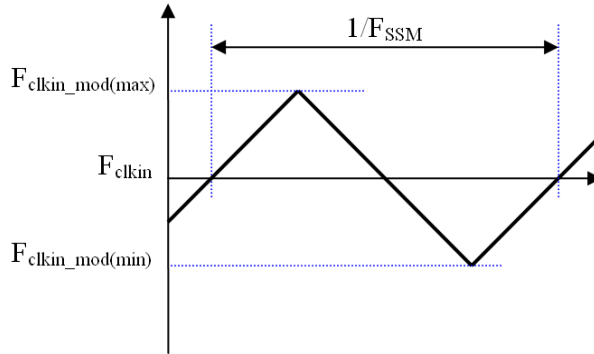
Note (a) The input clock cycle-to-cycle jitter is defined as below figures. $Trcl = |T_1 - T_1|$



Note (b) Input Clock to data skew is defined as below figures.

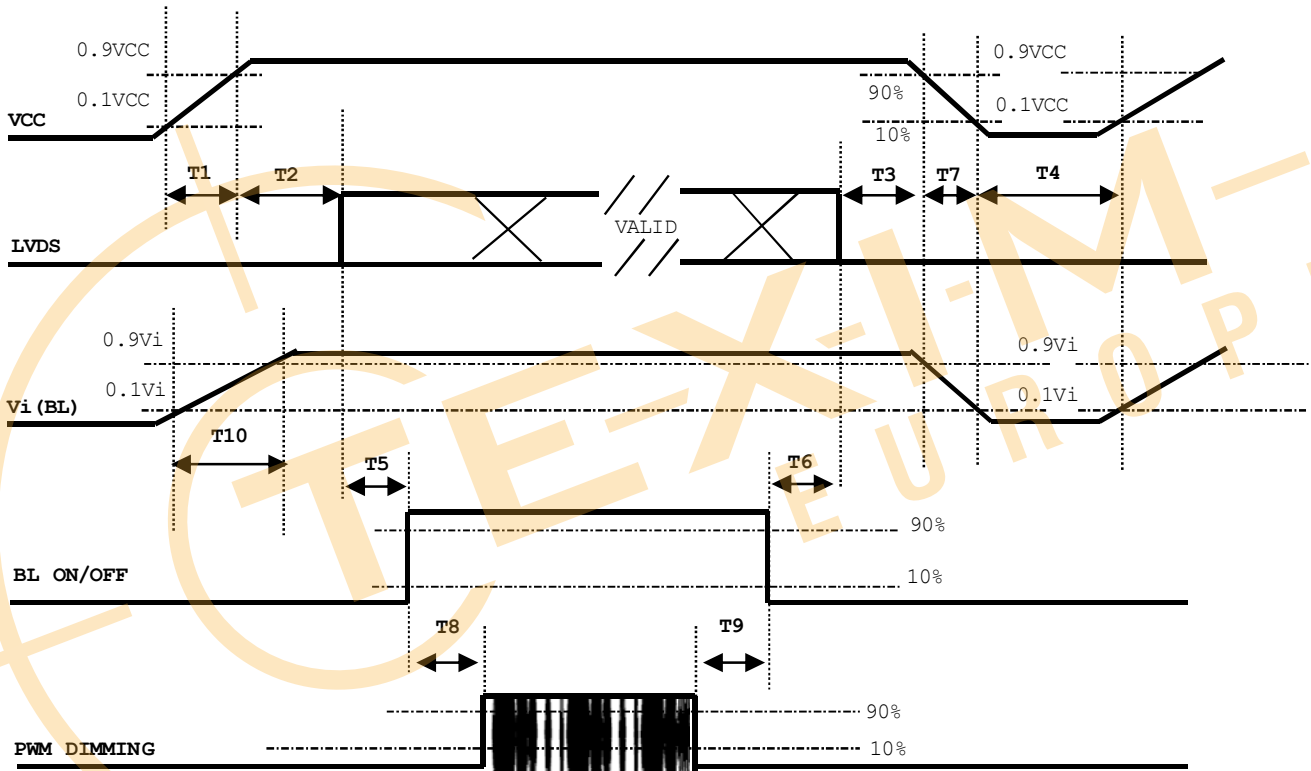


Note (c) The SSCG (Spread spectrum clock generator) is defined as below figures.



6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram below.



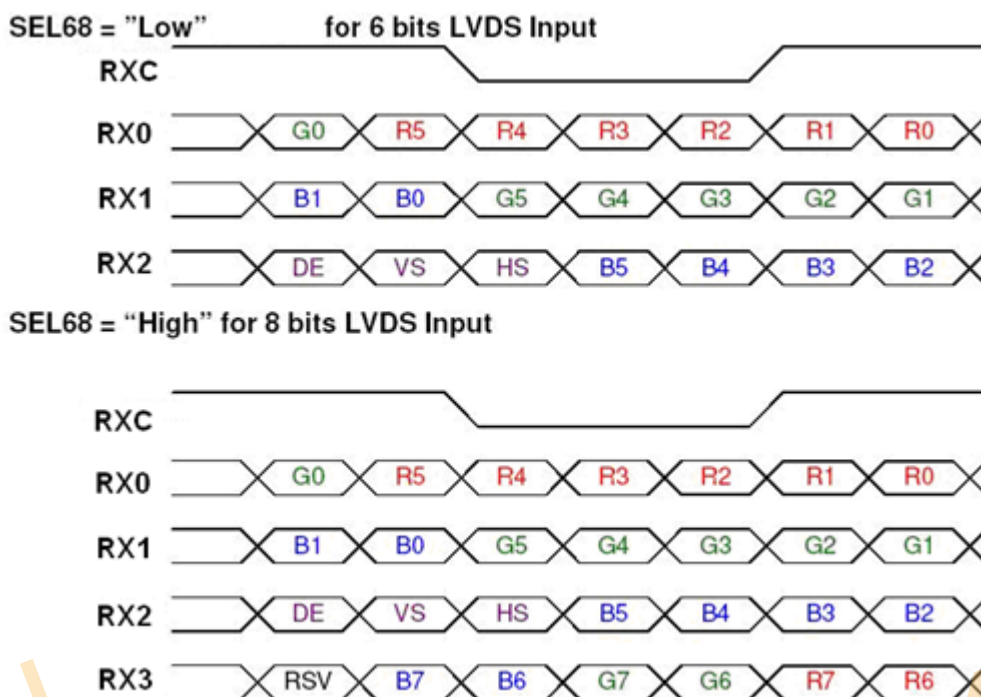
Note:

- (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2) When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.
- (3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.

- (5) Interface signal shall not be kept at high impedance when the power is on.
- (6) INX won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.
- (7) There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "T7 spec".

Parameter	Value			Units
	Min	Typ	Max	
T1	0.5	---	10	ms
T2	0	---	50	ms
T3	0	---	50	ms
T4	500	---	---	ms
T5	450	---	---	ms
T6	200	---	---	ms
T7	10	---	100	ms
T8	10	---	---	ms
T9	10	---	---	ms
T10	20	---	50	ms

6.3 THE INPUT DATA FORMAT



Note (1) R/G/B data 7: MSB, R/G/B data 0: LSB

Note (2) Please follow PSWG

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
R6	Red Data 6	
R5	Red Data 5	
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
G6	GreenData 6	
G5	GreenData 5	
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
B6	Blue Data 6	
B5	Blue Data 5	
B4	Blue Data 4	
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RXCLKIN+ RXCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note (3) Output signals from any system shall be low or Hi-Z state when VCC is off.

6.4 SCANNING DIRECTION

The following figures show the image see from the front view. The arrow indicates the direction of scan.

Fig.1 Normal Scan



Fig.2 Reverse Scan



Fig.3 Reverse Scan



Fig.4 Reverse Scan



Fig. 1 Normal scan (pin 17, reLR = Low , pin 18, reUD = Low)

Fig. 2 Reverse scan (pin 17, reLR = High, pin 18, reUD = Low)

Fig. 3 Reverse scan (pin 17, reLR = Low , pin 18, reUD = High)

Fig. 4 Reverse scan (pin 17, reLR = High, pin 18, reUD = High)

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	oC
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	According to typical value and tolerance in "ELECTRICAL CHARACTERISTICS"		
Input Signal			
PWM Duty Ratio	D	100	%

7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2 and all items are measured at the center point of screen except white variation. The following items should be measured under the test conditions described in above and stable environment shown in Note (5).

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Color Chromaticity	Red	Rx	$\theta_X=0^\circ, \theta_Y=0^\circ$ Grayscale Maximum	0.602	0.652	0.702	-	(1), (5)
		Ry		0.288	0.338	0.388	-	
	Green	Gx		0.274	0.324	0.374	-	
		Gy		0.557	0.607	0.657	-	
	Blue	Bx		0.103	0.153	0.203	-	
		By		0	0.048	0.098	-	
	White	Wx		0.263	0.313	0.363	-	
		Wy		0.279	0.329	0.379	-	
Center Luminance of White		L _C		480	600	-	cd/m ²	(4), (5)
Contrast Ratio		CR		700	1000	-	-	(2), (5)
Response Time		T _R	$\theta_x=0^\circ, \theta_y=0^\circ$	-	13	18	ms	(3)
		T _F		-	12	17	ms	
White Variation		δW	$\theta_x=0^\circ, \theta_y=0^\circ$	-	1.25	1.4	-	(5), (6).
Viewing Angle	Horizontal	θ_{x+}	CR≥10	85	89	-	Deg.	(1), (5)
		θ_{x-}		85	89	-		
	Vertical	θ_{y+}		85	89	-		
		θ_{y-}		85	89	-		

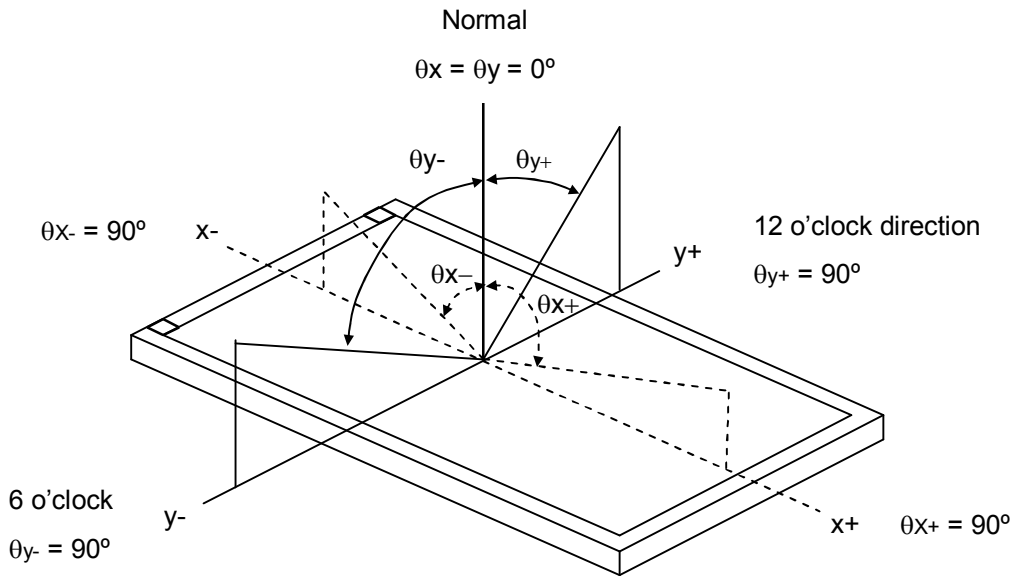
Definition :

Grayscale Maximum : Grayscale 255 (10 bits: grayscale 1023 ; 8 bits : grayscale 255 ; 6 bits: grayscale 63)

White : Luminance of Grayscale Maximum (All R,G,B)

Black : Luminance of grayscale 0 (All R,G,B).

Note (1) Definition of Viewing Angle (θ_x, θ_y):

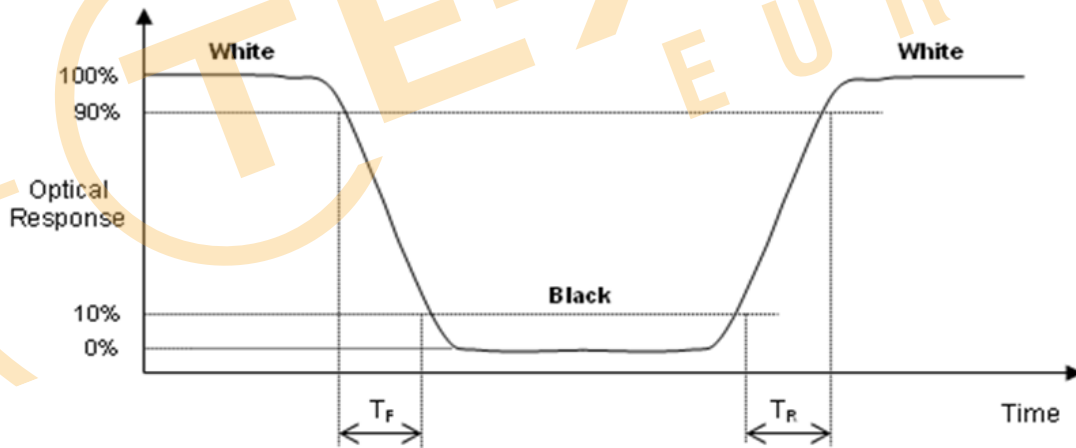


Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = \text{White} / \text{Black}$$

Note (3) Definition of Response Time (T_R, T_F):

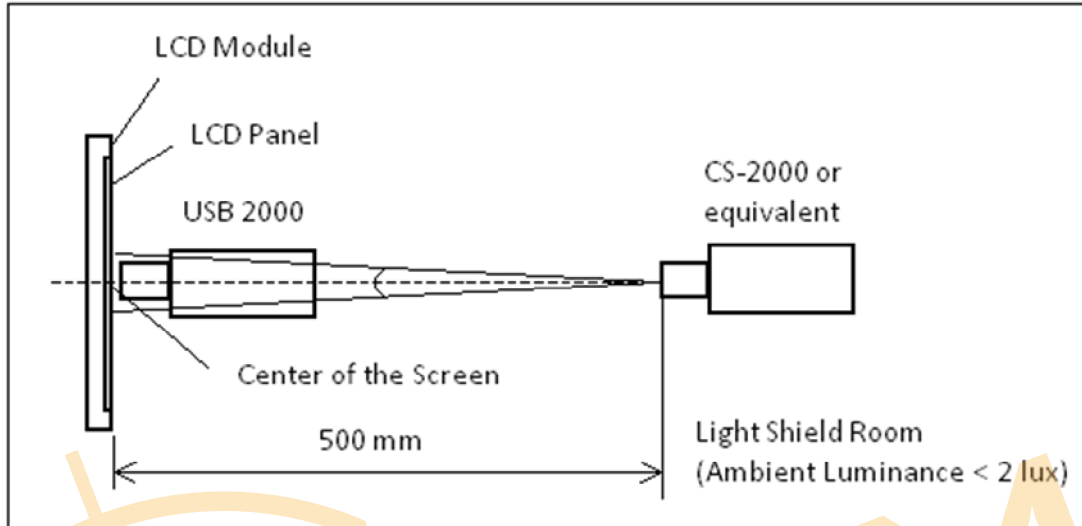


Note (4) Definition of Luminance of White (L_C):

Measure the luminance of White at center point

Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 40 minutes in a windless room. The measurement placement of module should be in accordance with the module drawing.

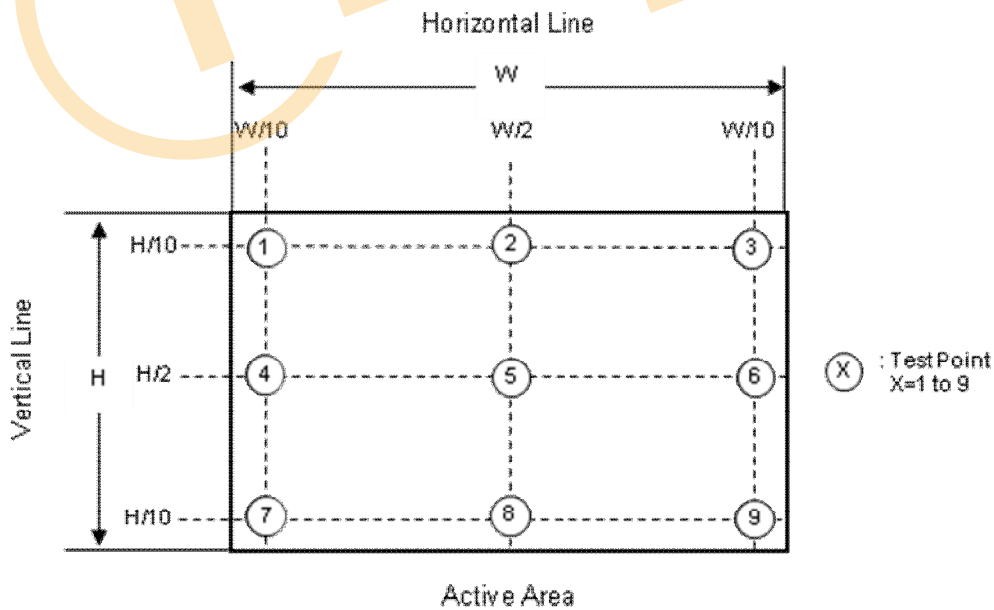


Note (6) Definition of White Variation (δW):

Measure the luminance of White at 9 points.

Luminance of White : $L(X)$, where X is from 1 to 9.

$$\delta W = \frac{\text{Minimum [L(1) to L(9)]}}{\text{Maximum [L(1) to L(9)]}} \times 100\%$$



8. RELIABILITY TEST CRITERIA

Test Item	Test Condition	Note
High Temperature Storage Test	90°C, 240 hours	(1)(2) (4)(5)
Low Temperature Storage Test	-40°C, 240 hours	
Thermal Shock Storage Test	-30°C, 0.5hour \longleftrightarrow 85°C, 0.5hour; 1hour/cycle,100cycles	
High Temperature Operation Test	85°C, 240 hours	
Low Temperature Operation Test	-30°C, 240 hours	
High Temperature & High Humidity Operation Test	60°C, 90%RH, 240hours	(1)(2) (4)(6)
Shock (Non-Operating)	200G, 2ms, half sine wave, 1 time for $\pm X, \pm Y, \pm Z$.	(2)(3)
Vibration (Non-Operating)	1.5G, 10 ~ 300 Hz, 10min/cycle, 3 cycles each X, Y, Z	(2)(3)

Note (1) There should be no condensation on the surface of panel during test.

Note (2) Temperature of panel display surface area should be 92 °C Max.

Note (3) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

Note (4) In the standard conditions, there is no function failure issue occurred. All the cosmetic specification is judged before reliability test.

Note (5) Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

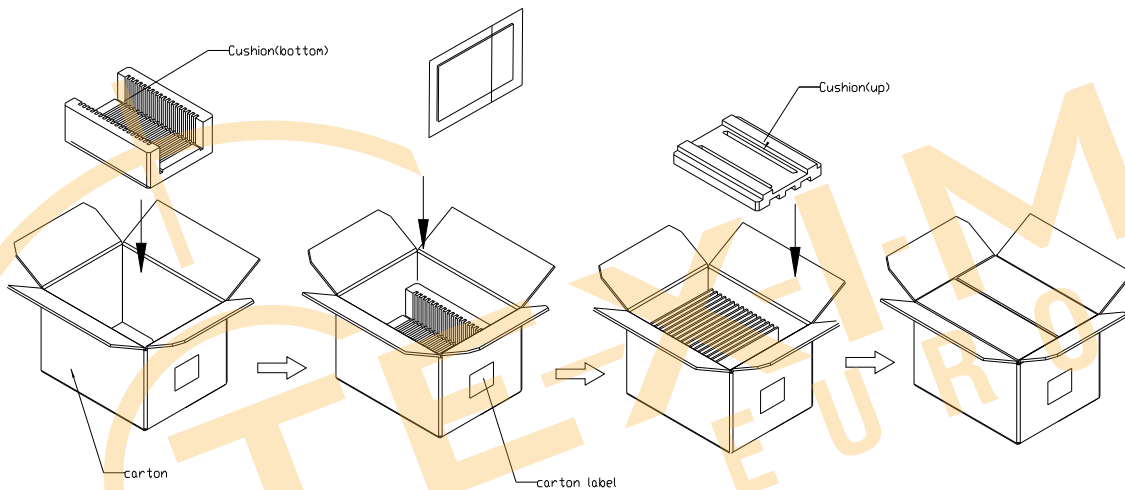
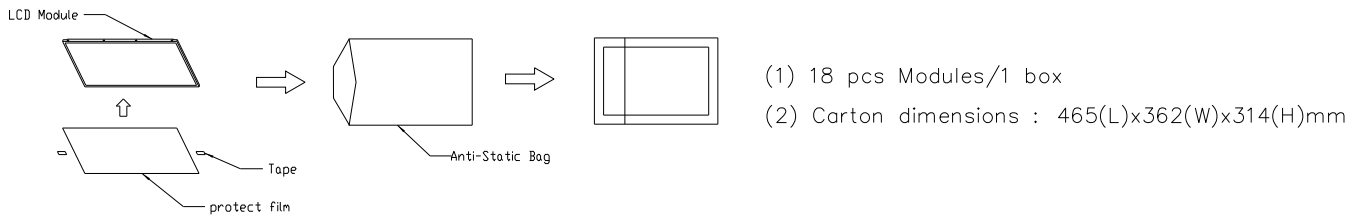
Note (6) Before cosmetic and function test, the product must have enough recovery time, at least 24 hours at room temperature.

9. PACKAGING

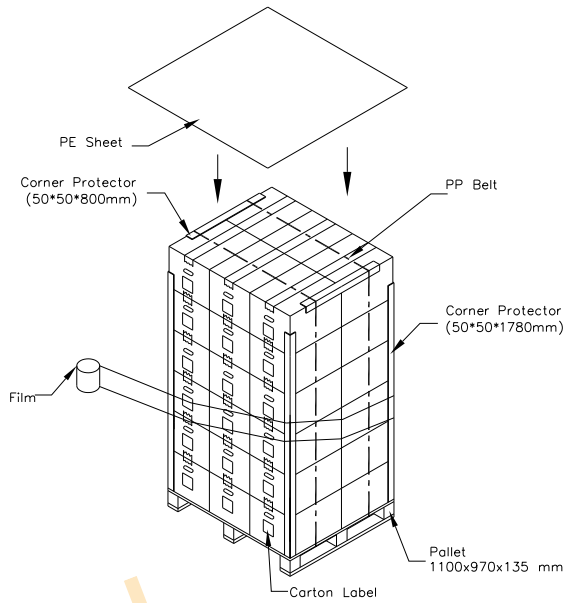
9.1 PACKING SPECIFICATIONS

- (1) 18pcs LCD modules / 1 Box
- (2) Box dimensions: 465 (L) X 362 (W) X 314 (H) mm
- (3) Weight: approximately 10.9Kg (18 modules per box)

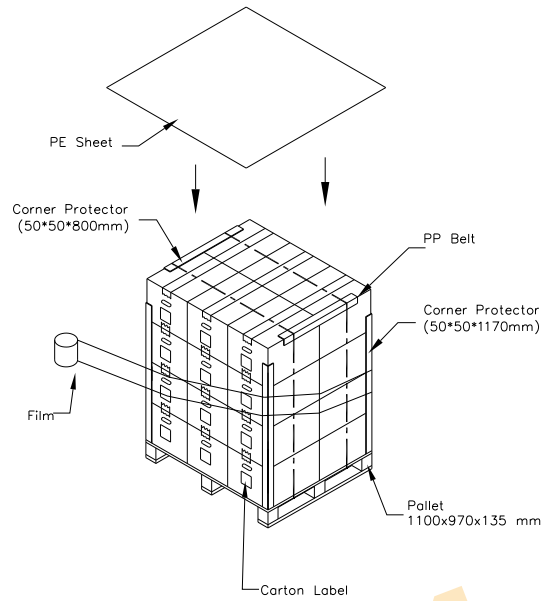
9.2 PACKING METHOD



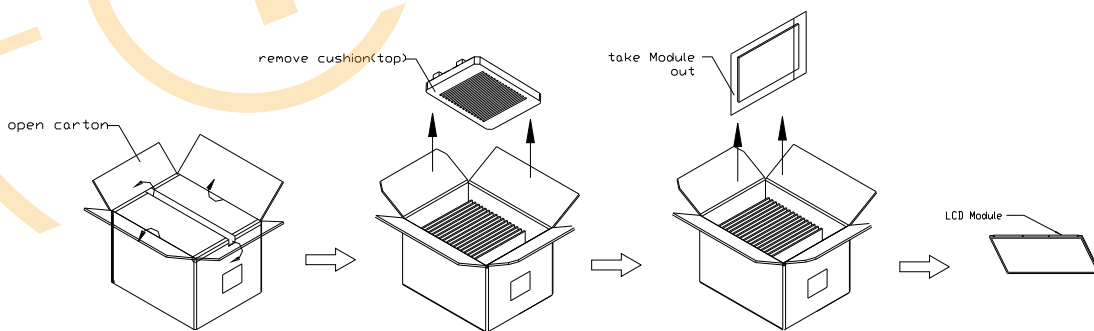
Sea / Land Transportation (40ft Container)



Air Transportation



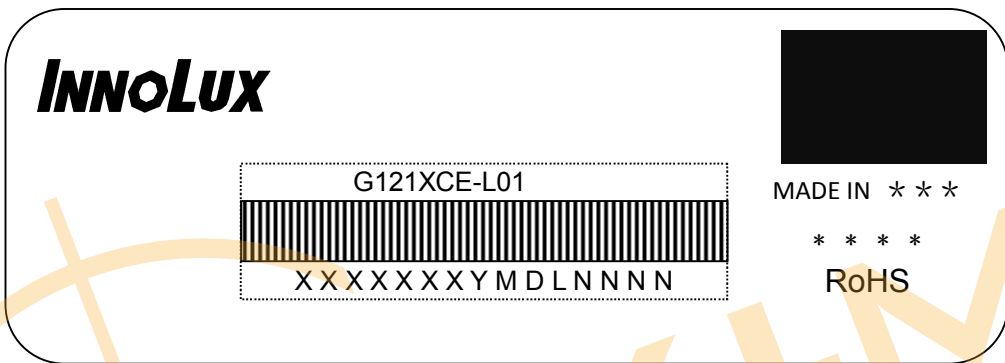
9.3 UN-PACKING METHOD



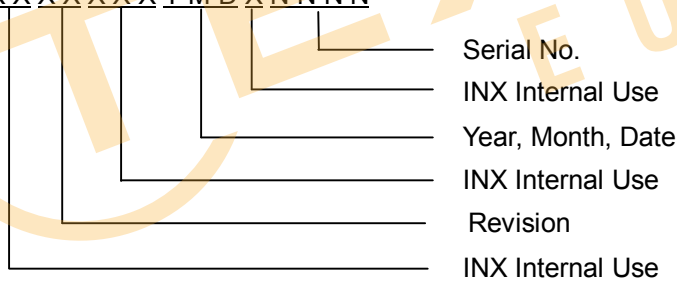
10. DEFINITION OF LABELS

10.1 MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: G121XCE- L01
- (b) * * * * : Factory ID
- (c) Serial ID: XXXXXXXYMDXNNNN



Serial ID includes the information as below:

- (a) Manufactured Date: Year: 1~9, for 2021~2029
 Month: 1~9, A~C, for Jan. ~ Dec.
 Day: 1~9, A~Y, for 1st to 31st, exclude I , O and U
- (b) Revision Code: cover all the change
- (c) Serial No.: Manufacturing sequence of product

11. PRECAUTIONS

11.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) The module should be assembled into the system firmly by using every mounting hole. Be careful not to twist or bend the module.
- (2) While assembling or installing modules, it can only be in the clean area. The dust and oil may cause electrical short or damage the polarizer.
- (3) Use fingerstalls or soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (4) Do not press or scratch the surface harder than a HB pencil lead on the panel because the polarizer is very soft and easily scratched.
- (5) If the surface of the polarizer is dirty, please clean it by some absorbent cotton or soft cloth. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanently damage the polarizer due to chemical reaction.
- (6) Wipe off water droplets or oil immediately. Staining and discoloration may occur if they left on panel for a long time.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contacting with hands, legs or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static electricity, it may cause damage to the C-MOS Gate Array IC.
- (9) Do not disassemble the module.
- (10) Do not pull or fold the lamp wire.
- (11) Pins of I/F connector should not be touched directly with bare hands.

11.2 STORAGE PRECAUTIONS

- (1) When storing for a long time, the following precautions are necessary.
 - (a) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 30°C at humidity 50+-10%RH.
 - (b) The polarizer surface should not come in contact with any other object.
 - (c) It is recommended that they be stored in the container in which they were shipped.
 - (d) Storage condition is guaranteed under packing conditions.
 - (e) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition
- (2) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (3) It is dangerous that moisture come into or contacted the LCD module, because the moisture may damage LCD module when it is operating.
- (4) It may reduce the display quality if the ambient temperature is lower than 10 °C. For example, the response time will become slowly, and the starting voltage of lamp will be higher than the room temperature.

11.3 OTHER PRECAUTIONS

(1) Normal operating condition

(a) Display pattern: dynamic pattern (Real display)

(Note) Long-term static display can cause image sticking.

(2) Operating usages to protect against image sticking due to long-term static display

(a) Suitable operating time: under 16 hours a day.

(b) Static information display recommended to use with moving image.

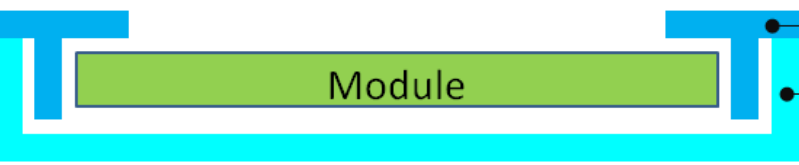
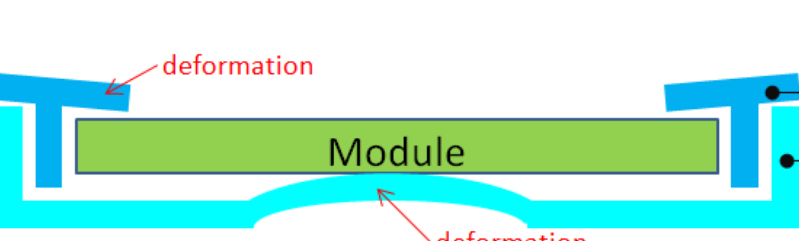
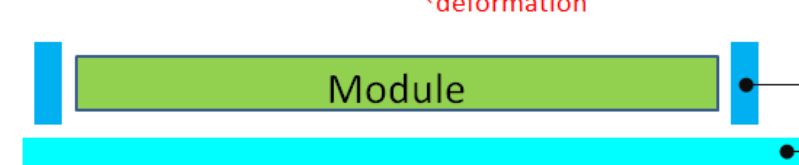
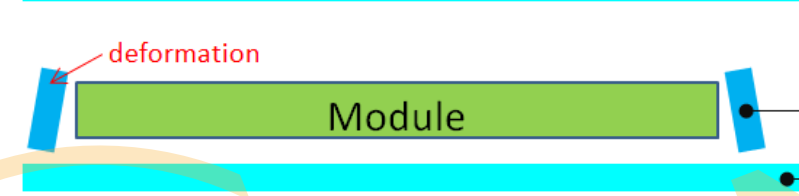
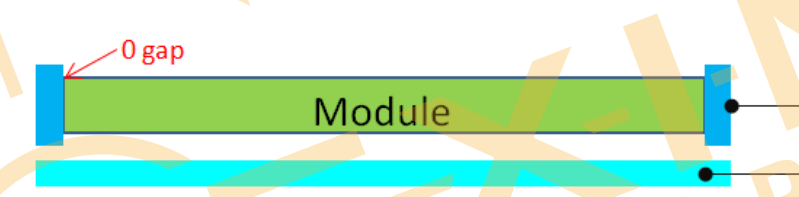
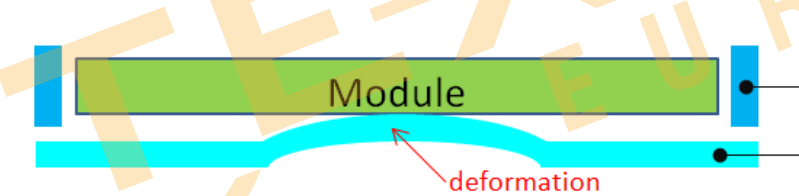
(c) Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.

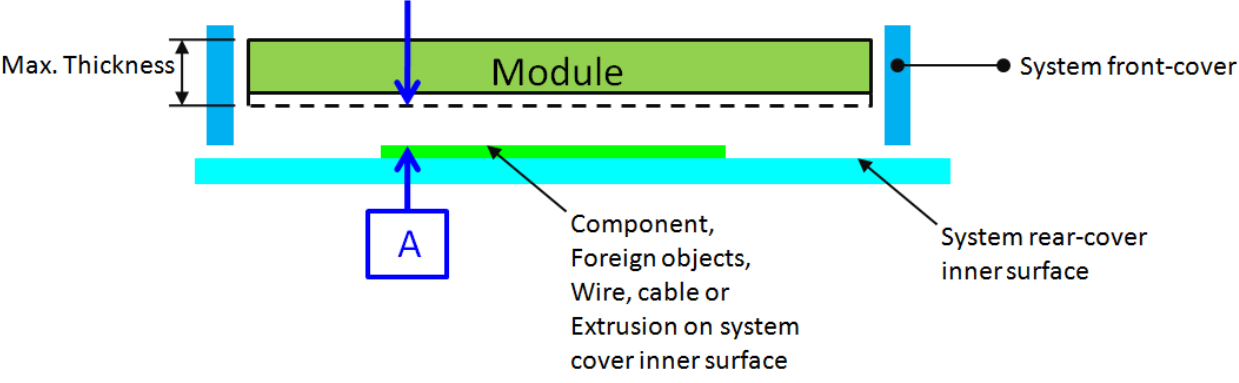
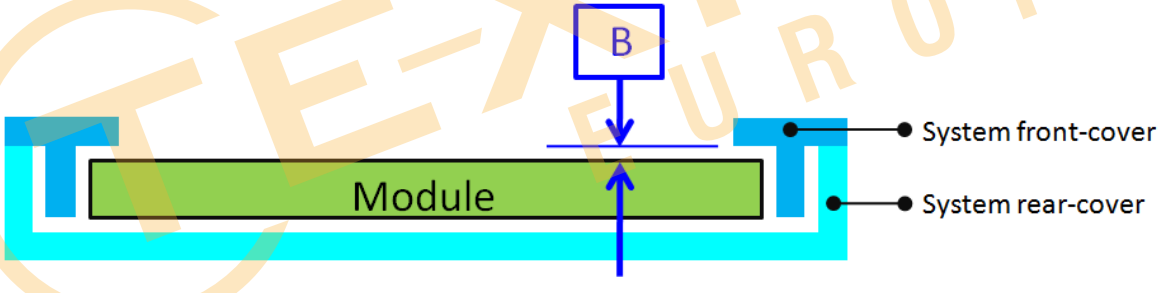
(3) Abnormal condition just means conditions except normal condition.



2	Tape/Sponge design on system inner surface
Definition	<p>a. To prevent from abnormal display & white spot after mechanical test, we suggest using Tape/Sponge as medium between chassis and Module rear cover could reduce the occurrence of white spot.</p> <p>b. When using the Tape/Sponge, we suggest it be lay over between set chassis and Module rear cover. It is not recommended to add Tape/Sponge in separate location. Since each Tape/Sponge may act as pressure concentration location.</p>

3	System inner surface examination
<p>The diagram illustrates the system inner surface examination. The top part shows a green 'Module' with a red-hatched 'Module PCBA' area. A label 'The hatch area' points to the hatched region. The bottom part shows a cross-section of the 'Module' (green) on a 'System cover inner surface' (cyan). Labels include 'Burr', 'PCBA', 'Chassis', and 'Step'.</p>	
Definition	<p>a. The hatch area on Module PCBA should keep at least 1mm gap(X,Y,Z direction) to any structure with system cover inner surface.</p> <p>b. Burr, Step, PCB protrusion may cause stress concentration. White spot may occur during reliability test.</p>

6	Permanent deformation of system cover after reliability test
○	 <ul style="list-style-type: none"> ● System front-cover ● System rear-cover
✗	 <ul style="list-style-type: none"> ● System front-cover ● System rear-cover
○	 <ul style="list-style-type: none"> ● System front-cover ● System rear-cover
✗	 <ul style="list-style-type: none"> ● System front-cover ● System rear-cover
✗	 <ul style="list-style-type: none"> ● System front-cover ● System rear-cover
✗	 <ul style="list-style-type: none"> ● System front-cover ● System rear-cover
Definition	<p>System cover including front cover and rear cover may deform during reliability test. Permanent deformation of system front cover and rear cover after reliability test should not interfere with panel. Because it may cause issue such as pooling, abnormal display, white spot and also cell creak.</p> <p>Note: If the interference cannot be avoided, please feel free to contract INX FAE Engineer for collaboration design. We can help to verify and pass risk assessment for customer reference.</p>

7	Design gap A between panel & any components on system rear-cover
	
Definition	<p>System cover including front cover and rear cover may deform during reliability test. Permanent deformation of system front cover and rear cover after reliability test should not interfere with panel. Because it may cause issue such as pooling, abnormal display, white spot and also cell creak.</p> <p>Note: If the interference cannot be avoided, please feel free to contract INX FAE Engineer for collaboration design. We can help to verify and pass risk assessment for customer reference.</p>
8	Design gap B between system front-cover & panel surface
	
Definition	<p>Gap between system front-cover & panel surface is needed to prevent pooling or glass broken. Zero gap or interference such as burr and warpage from mold frame may cause pooling issue near system front-cover opening edge. This phenomenon is obvious during swing test, hinge test, knock test or during pooling inspection procedure.</p> <p>To remain sufficient gap, design with system rib higher than maximum panel thickness is recommended.</p> <p>Note: If the interference cannot be avoided, please feel free to contract INX FAE Engineer for collaboration design. We can help to verify and pass risk assessment for customer reference.</p>

9	Design gap C between panel & system front-cover or protrusions
Definition	<p>Gap between panel & system front-cover or protrusions is needed to prevent shock test failure. Because system front-cover or protrusions with small gap may hit panel during the test. Issue such as cell crack, abnormal display may occur.</p> <p>The gap should be large enough to absorb the maximum displacement during the test.</p> <p>Note: If the interference cannot be avoided, please feel free to contract INX FAE Engineer for collaboration design. We can help to verify and pass risk assessment for customer reference.</p>

10	Design distance between TP AA to LCD AA
Definition	TP VA should avoid TP ink area covering LCD AA or causing the module frame to be exposed.

11	Use OCR Lamination
Definition	<ol style="list-style-type: none"> 1.OCR glue as possible beyond module, in order to avoid Line Pooling 2.Add side glue to avoid Line Pooling

Disclaimer

ALL PRODUCTS, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Texim Europe B.V. its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Texim"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Texim makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product.

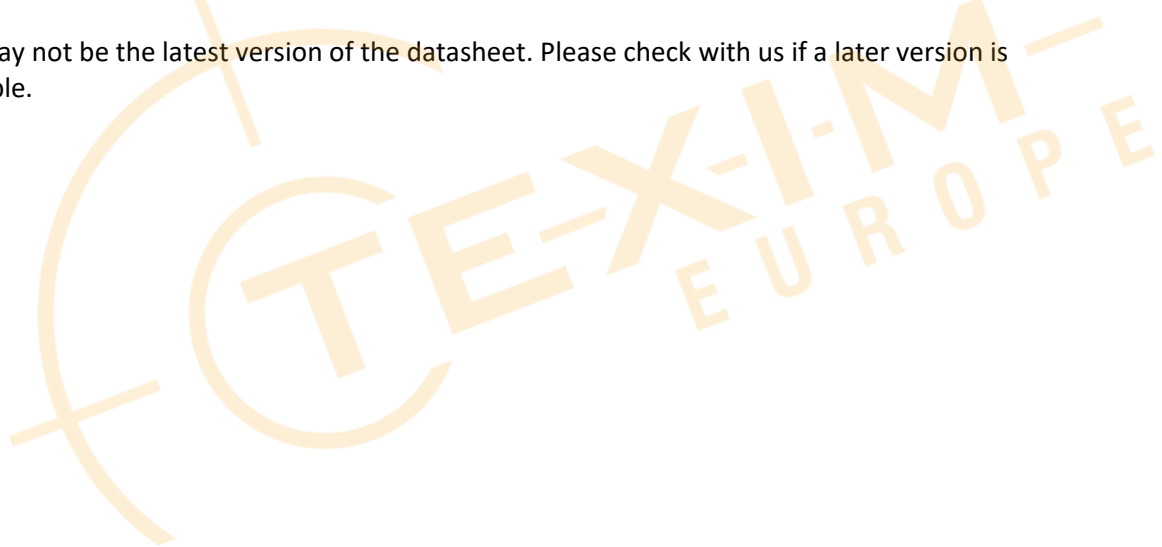
It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application.

Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time.

All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts.

Please contact us if you have any questions about the contents of the datasheet.

This may not be the latest version of the datasheet. Please check with us if a later version is available.





Headquarters & Warehouse

Elektrostraat 17
NL-7483 PG Haaksbergen
The Netherlands

T: +31 (0)53 573 33 33
E: info@texim-europe.com
Homepage: www.texim-europe.com



The Netherlands

Elektrostraat 17
NL-7483 PG Haaksbergen

T: +31 (0)53 573 33 33
E: nl@texim-europe.com



Belgium

Zuiderlaan 14, box 10
B-1731 Zellik

T: +32 (0)2 462 01 00
E: belgium@texim-europe.com



UK & Ireland

St Mary's House, Church Lane
Carlton Le Moorland
Lincoln LN5 9HS

T: +44 (0)1522 789 555
E: uk@texim-europe.com



Germany - North

Bahnhofstrasse 92
D-25451 Quickborn

T: +49 (0)4106 627 07-0
E: germany@texim-europe.com



Germany - South

Martin-Kollar-Strasse 9
D-81829 München

T: +49 (0)89 436 086-0
E: muenchen@texim-europe.com



Austria

Warwitzstrasse 9
A-5020 Salzburg

T: +43 (0)662 216 026
E: austria@texim-europe.com



Nordic

Søndre Jagtvej 12
DK-2970 Hørsholm

T: +45 88 20 26 30
E: nordic@texim-europe.com



Italy

Martin-Kollar-Strasse 9
D-81829 München

T: +49 (0)89 436 086-0
E: italy@texim-europe.com