

# SPECIFICATION FOR CTP+LCD MODULE

**Part No.: TF070TM023-V1** 

**Customer No.:** 

Date: 2024-02-28

|          | Supplier | Customer |          |
|----------|----------|----------|----------|
| Prepared | Checked  | Approved | Approved |
|          |          |          |          |

# **Record of Revision**

| Rev. | Description                        | Page | Date     |
|------|------------------------------------|------|----------|
| 0    | Preliminary specification released |      | 20240228 |
|      |                                    |      |          |
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# 1 General Specifications

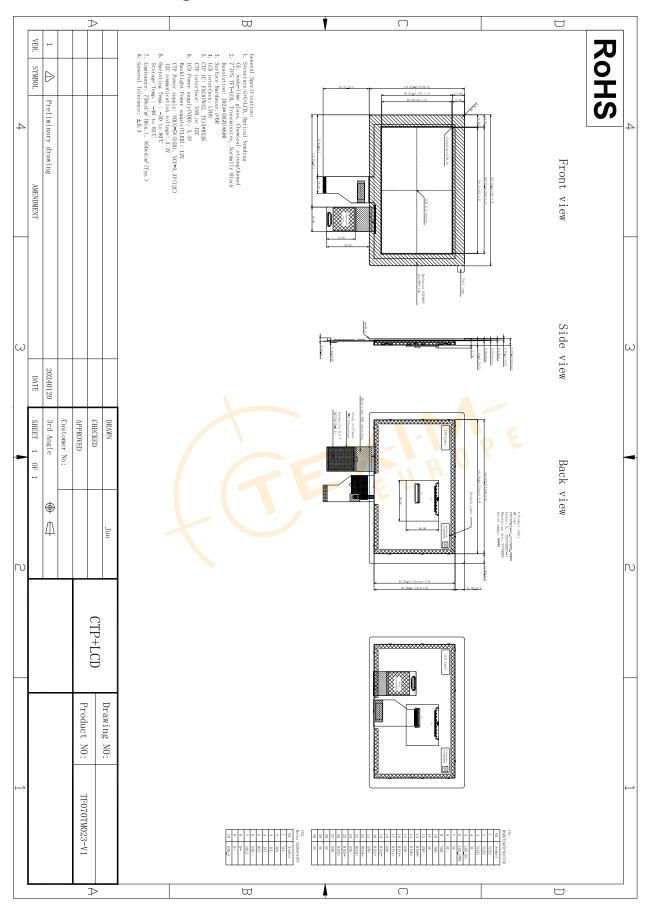
## 1.1 Definition

The specification is used for capacitive touch module with TFT LCD & Backlight Unit.

# 1.2 Features and General Description

| General            | Specification                   | Unit    | Note     |
|--------------------|---------------------------------|---------|----------|
| Module size        | 7.0                             | inch    | diagonal |
| Structure          | GG+LCD. Optical Bonding         |         |          |
| Resolution         | 1024(RGB) × 600                 |         |          |
| Display Mode       | Normally Black. Transmissive    |         |          |
| Pixel Pitch        | $0.1506(W) \times 0.1432(H)$    | mm      |          |
| Pixel Arrangement  | RGB stripe                      |         |          |
| Viewing direction  | ALL                             | O'clock |          |
| Outline dimensions | 185.21(W) × 116.92(H) × 8.35(D) | mm      | W/O PCBA |
| Active area        | 154.21(W) × 85.92(H)            | mm      |          |
| LCD interface      | LVDS 8bit                       |         |          |
| CTP Interface      | USB or I2C                      |         |          |
| CTP IC             | EXC81W32                        | E       |          |
| Touch point        | 10                              | P       |          |
| Surface hardness   | 6                               | Н       |          |

# 2 Mechanical Drawing



# 3 Pin assignment

# 3.1 LCD Pin assignment

CN1 Module Side Connector: STM MSBKT2407P30 or Compatible

User Side Connector: JAE FI-X30H

| Pin No. | Symbol   | Description                               |
|---------|----------|---|
| 1       | VLED     | Backlight power supply: +12V              |
| 2       | VLED     | Backlight power supply: +12V              |
| 3       | VLED     | Backlight power supply: +12V              |
| 4       | NC       | Not connection                            |
| 5       | LED_EN   | Backlight on/off control                  |
| 6       | LED_PWM  | Backlight dimming control                 |
| 7       | GND      | Power Ground                              |
| 8       | NC       | Not connection                            |
| 9       | VDD      | LCD power Supply: +3.3V                   |
| 10      | VDD      | LCD power Supply: +3.3V                   |
| 11      | NC       | Not connection                            |
| 12      | GND      | Power Ground                              |
| 13      | RXIN0-   | Negative LVDS differential data input (0) |
| 14      | RXIN0+   | Positive LVDS differential data input (0) |
| 15      | GND      | Power Ground                              |
| 16      | RXIN1-   | Negative LVDS differential data input (1) |
| 17      | RXIN1+   | Positive LVDS differential data input (1) |
| 18      | GND      | Power Ground                              |
| 19      | RXIN2-   | Negative LVDS differential data input (2) |
| 20      | RXIN2+   | Positive LVDS differential data input (2) |
| 21      | GND      | Power Ground                              |
| 22      | RXCLKIN- | Negative LVDS differential clock input    |
| 23      | RXCLKIN+ | Negative LVDS differential clock input    |
| 24      | GND      | Power Ground                              |
| 25      | RXIN3-   | Negative LVDS differential data input (3) |
| 26      | RXIN3+   | Positive LVDS differential data input (3) |
| 27      | GND      | Power Ground                              |
| 28      | NC       | Not connection                            |
| 29      | NC       | Not connection                            |
| 30      | NC       | Not connection                            |

# 3.2 CTP Pin assignment

**CN2** Module Side Connector : Molex 53216-1071 or Compatible

User Side Connector: Molex 51021-1000

| Pin No. | Symbol | Description             |
|---------|--------|-------------------------|
| 1       | VCC    | I2C Power Supply: +3.3V |
| 2       | SDA    | I2C data signal         |
| 3       | SCL    | I2C clock signal        |
| 4       | INT    | I2C interrupt signal    |
| 5       | RST    | Reset pin, active low   |
| 6       | GND    | Power Ground            |
| 7       | VBUS   | USB Power Supply: +5V   |
| 8       | D-     | USB data port minus     |
| 9       | D+     | USB data port plus      |
| 10      | GND_E  | Power Ground            |



# 4 Absolute Maximum Rating

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table

| Parameter             | Symbol   | Min. | Max. | Unit |
|-----------------------|----------|------|------|------|
|                       | VDD      | -0.3 | 4.0  | V    |
| Power Supply Voltage  | VCC      | -0.3 | 3.6  | V    |
|                       | VBUS     | -0.5 | 5.5  | V    |
| Operating Temperature | Тор      | -30  | +80  | °C   |
| Storage Temperature   | $T_{ST}$ | -40  | +85  | °C   |



#### **5** Electrical Characteristic

## 5.1 Driving LCD Panel

| Dayamatay            | Cymbol           | Values |      |      | Unit | Domaylı |
|----------------------|------------------|--------|------|------|------|---------|
| Parameter            | Symbol           | Min.   | Тур. | Max. | Unit | Remark  |
| Power Supply Voltage | VDD              | 3.0    | 3.3  | 3.6  | V    | Ta=25°C |
| Power Supply Current | I <sub>VDD</sub> | -      | TBD  | TBD  | mA   |         |

## 5.2 CTP recommended Operating Condition

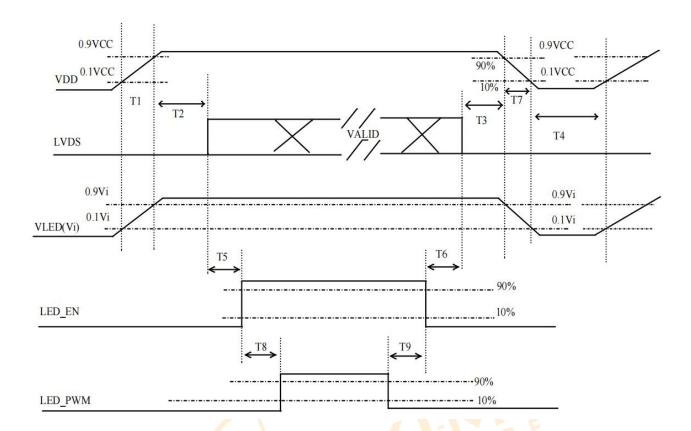
| Dayamatay              | Cymbol            | Values |      |      | Unit | Remark |
|------------------------|-------------------|--------|------|------|------|--------|
| Parameter              | Symbol            | Min.   | Тур. | Max. | Unit | Kemark |
| Dayrag Camala Valta as | VCC               | 3.0    | 3.3  | 3.6  | V    | T25°C  |
| Power Supply Voltage   | VBUS              | 4.7    | 5.0  | 5.3  | V    | Ta=25℃ |
| Dayyon Cymaly, Cymant  | I <sub>VCC</sub>  | -      | TBD  | TBD  | mA   |        |
| Power Supply Current   | I <sub>VBUS</sub> | -      | TBD  | TBD  | mA   |        |

## 5.3 Driver LCD Backlight

| Parameter            |               | Symbol            | Min.  | Тур. | Max.  | Unit | Remarks |
|----------------------|---------------|-------------------|-------|------|-------|------|---------|
| Power Supply Voltage |               | VLED              | 10    | 12   | 15    | V    | Ta=25°C |
| Power Supp           | oly Current   | I <sub>VLED</sub> | -     | 0.4  | 0.5   | A    |         |
| EN Signal            | High Level    |                   | 1.65  | -    | 5.25  | V    |         |
| Voltage              | Low Level     | -                 | 0     | -    | 0.4   | V    |         |
| PWM                  | High Level    |                   | 1.65  | -    | 5.25  | V    |         |
| Control Level        | Low Level     | -                 | 0     | -    | 0.4   | V    |         |
| PWM Fr               | PWM Frequency |                   | 100   | -    | 20000 | Hz   |         |
| PWM Dimming duty     |               | ı                 | 5     | -    | 100   | %    |         |
| LED Life Time        |               | -                 | 30000 | -    | -     | Hour | Note 1  |
| Power Con            | nsumption     | $P_{LED}$         | -     | 6    | -     | W    |         |

Note 1: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=55mA on condition of continuous operating at 25±2°C

#### 5.4 Power on sequence



| D 4       |      | Value     |     |      |  |  |  |
|-----------|------|-----------|-----|------|--|--|--|
| Parameter | Min. | Min. Typ. |     | Unit |  |  |  |
| T1        | 0.5  | -         | 10  | ms   |  |  |  |
| T2        | 0    | -         | 50  | ms   |  |  |  |
| Т3        | 0    | -         | 50  | ms   |  |  |  |
| T4        | 500  | -         | -   | ms   |  |  |  |
| Т5        | 200  | -         | -   | ms   |  |  |  |
| Т6        | 20   | -         | -   | ms   |  |  |  |
| T7        | 5    | -         | 300 | ms   |  |  |  |
| Т8        | 10   | -         | -   | ms   |  |  |  |
| Т9        | 10   | -         | -   | ms   |  |  |  |

Note 1: Please avoid floating state of interface signal at invalid period.

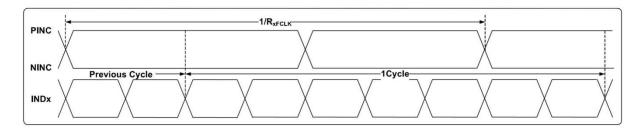
Note 2: When the interface signal is invalid, be sure to pull down the power supply of LCD VDD to 0 V.

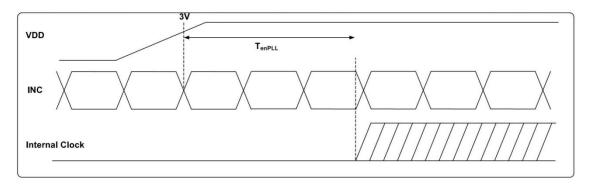
Note 3: The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.

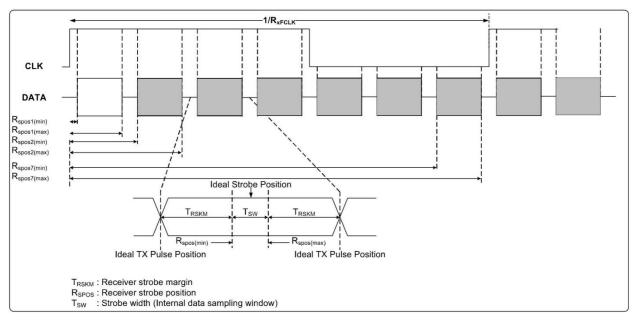
# 6 Signal Timing Specification

## **6.1 AC Electrical Characteristics**

| Parameter              | Symbol             | Spec. |                            |      | Unit  | Condition   |  |
|------------------------|--------------------|-------|----------------------------|------|-------|---|--|
| Farailletei            | Syllibol           | Min.  | Тур.                       | Max. | Offic | Condition   |  |
| Clock frequency        | R <sub>XFCLK</sub> | 20    | -                          | 71   | MHz   | -   |  |
| Input data skew margin | T <sub>RSKM</sub>  | 500   | -                          | -    | pS    | V <sub>ID</sub>  =400mV<br>R <sub>XVCM</sub> =1.2V<br>R <sub>XFCLK</sub> =71MHz |  |
| Clock high time        | T <sub>LVCH</sub>  | -     | 4/(7* R <sub>XFCLK</sub> ) | -    | ns    | -   |  |
| Clock low time         | T <sub>LVCL</sub>  | -     | 3/(7* R <sub>XFCLK</sub> ) | -    | ns    | -   |  |
| PLL wake-up time       | T <sub>emPLL</sub> | -     | -                          | 150  | μs    | =   |  |







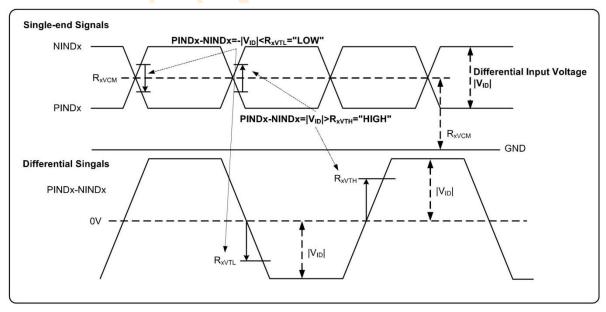
| Parameter            | Symbol            |      | Spec. |      | Unit  | Condition                       |  |
|----------------------|-------------------|------|-------|------|-------|---------------------------------|--|
| Parameter            | Syllibol          | Min. | Тур.  | Max. | Ullit | Condition                       |  |
| Modulation Frequency | SSC <sub>MF</sub> | 23   | -     | 93   | KHz   | -                               |  |
| Modulation Rate      | SSC <sub>MR</sub> | -    | -     | ±3   | %     | LVDS clock =71MHz center spread |  |

**SCC** table

#### **6.2** DC electrical characteristics

| Parameter                                    | Symbol             |                     | Spec. |                                  | Unit  | Condition                         |
|--|--------------------|---------------------|-------|----------------------------------|-------|-----------------------------------|
| Parameter                                    | Syllibol           | Min.                | Тур.  | Max.                             | Ullit | Condition                         |
| Differential input high<br>Threshold voltage | R <sub>XVTH</sub>  | :-                  | -     | +0.1                             | V     | R <sub>XVCM</sub> =1.2V           |
| Differential input low threshold voltage     | R <sub>XVTL</sub>  | -0.1                | -     | -                                | V     |                                   |
| Input voltage range (singled-end)            | R <sub>XVIN</sub>  | 0                   | -     | VDD-1.2+<br> V <sub>ID</sub>  /2 | V     | -                                 |
| Differential input common<br>Mode voltage    | R <sub>XVCM</sub>  | V <sub>ID</sub>  /2 | -     | VDD-1.2                          | V     | -                                 |
| Differential input voltage                   | V <sub>ID</sub>    | 0.2                 | -     | 0.6                              | V     | -                                 |
| Differential input leakage<br>Current        | RV <sub>Xliz</sub> | -10                 | -     | +10                              | μA    | -                                 |
| LVDS Digital Operating<br>Current            | Iddlvds            | -                   | 15    | 30                               | mA    | Fclk=65MHz, VDD=3.3V              |
| LVDS Digital Stand-by<br>Current             | Istlvds            | -                   | 10    | 50                               | μΑ    | Clock & all Functions are stopped |

LVDS mode DC Electrical Characteristics



Single-end signals

# 6.3 Timing Table

## DE mode

| Parameter               | Symbol     |      | Unit |      |                |
|-------------------------|------------|------|------|------|----------------|
| Farameter               | Syllibol   | Min. | Тур. | Max. | Ollit          |
| DCLK Frequency          | fclk       | 40.8 | 51.2 | 67.2 | MHz            |
| Horizontal Display Area | thd        |      | 1024 |      | DCLK           |
| HSD Period              | th         | 1114 | 1344 | 1400 | DCLK           |
| HSD Blanking            | thb+ thfp  | 90   | 320  | 376  | DCLK           |
| Vertical Display Area   | tvd        |      | 600  |      | T <sub>H</sub> |
| VSD Period              | tvbp       | 610  | 635  | 800  | T <sub>H</sub> |
| VSD Blanking            | tvbp+ tvfp | 10   | 35   | 200  | T <sub>H</sub> |

## HV mode

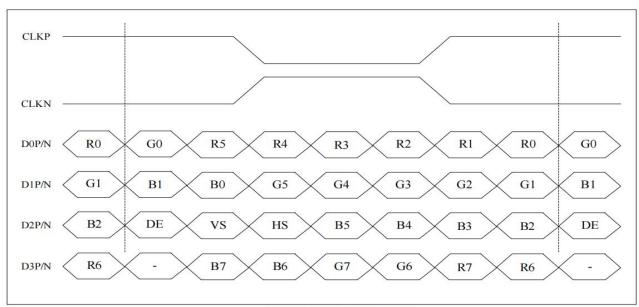
**Horizontal timing** 

| Parameter               | Symbol |      | Unit |      |      |
|-------------------------|--------|------|------|------|------|
| Farameter               | Symbol | Min. | Тур. | Max. | Oint |
| DCLK Frequency          | fclk   | 44.9 | 51.2 | 63   | MHz  |
| Horizontal Display Area | thd    |      | 1024 |      | DCLK |
| HSD Period              | th     | 1200 | 1344 | 1400 | DCLK |
| HSD Pulse Width         | thpw   | 1    | -    | 140  | DCLK |
| HSD Back Porch          | thbp   |      | 160  |      | DCLK |
| HSD Front Porch         | thfp   | 16   | 160  | 216  | DCLK |

**Vertical Timing** 

| Parameter             | Symbol |      | Unit |      |                |
|-----------------------|--------|------|------|------|----------------|
| Parameter             | Symbol | Min. | Тур. | Max. | Offic          |
| Vertical Display Area | tvd    |      | 600  |      | T <sub>H</sub> |
| VSD Period            | tv     | 624  | 635  | 750  | T <sub>H</sub> |
| VSD Pulse Width       | tvpw   | 1    | -    | 20   | T <sub>H</sub> |
| VSD Back Porch        | tvbp   |      | 23   |      | T <sub>H</sub> |
| VSD Front Porch       | tvfp   | 1    | 12   | 127  | T <sub>H</sub> |

## **6.4 LVDS Data Input Format**



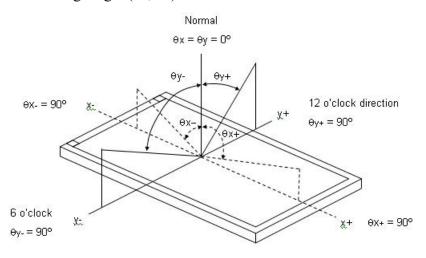
8-bit LVDS input



# 7 Optical Characteristics

| Iten                    | n          | Symbol                    | Condition   | Min.         | Тур. | Max.         | Unit | Note     |
|-------------------------|------------|---------------------------|---|--------------|------|--------------|------|----------|
|                         | Horizontal | $\theta_X$ +              |   | 80           | -    | - Deg        |      |          |
| Viewing                 | Попиона    | $\theta_{	ext{X}}$ -      | CD > 10   | 80           | -    | -            | Deg. | (1) (5)  |
| Angle                   | Vertical   | $\theta_{\mathrm{Y}}$ +   | CR≥10   | 80           | _    | -            | Deg. | (1), (5) |
|                         | vertical   | $	heta_{	ext{Y}}$ -       |   | 80           | -    | -            | Deg. |          |
| Luminance (W            | (/ CTP)    | Lv                        |   | 750          | 850  | -            | nit  | (4), (5) |
| Uniformity              | 9 points   | ΔΥ9                       | $\theta_X=0^\circ, \theta_Y=0^\circ$                | 70           | -    | -            | %    | (5), (6) |
| Contrast ratio          |            | CR                        |   | 900          | 1000 | -            |      | (2), (5) |
|                         | White      | $W_{x}$                   | $\theta_X$ =0°, $\theta_Y$ =0° R=G=B=255 Gray scale | Typ-<br>0.05 | 0.30 | Typ+<br>0.05 | -    | (1), (5) |
|                         |            | $W_{y}$                   |   |              | 0.33 |              | -    |          |
| G 1                     | Red        | $R_x$                     |   |              | -    |              | -    |          |
| Claramaticity           |            | $R_y$                     |   |              | -    |              | -    |          |
| Chromaticity (CIE 1931) | Casaa      | $G_{x}$                   |   |              | -    |              | -    |          |
| (CIE 1931)              | Green      | $G_{y}$                   |   |              | -    |              | -    |          |
|                         | D1         | $B_x$                     |   |              | -    |              | -    |          |
|                         | Blue       | $\mathbf{B}_{\mathbf{y}}$ |   |              | -    |              | -    |          |
| Response<br>Time        | $T_R+T_F$  |                           |   | -            | 25   | 35           | ms   | (3)      |

Note (1) Definition of Viewing Angle ( $\theta_X$ ,  $\theta_Y$ ):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L255 / L0

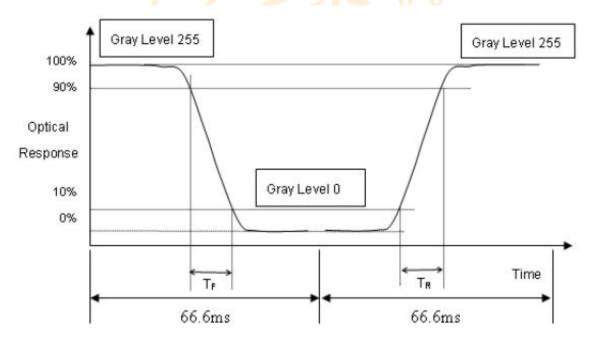
L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR(5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time  $(T_R, T_F)$ :



#### Note (4) Definition of Luminance of White (LC):

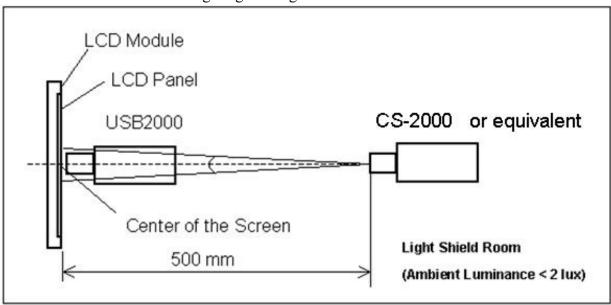
Measure the luminance of gray level 255 at center point

$$LC = L(5)$$

L (x) is corresponding to the luminance of the point X at Figure in Note (6).

#### Note (5) Measurement Setup:

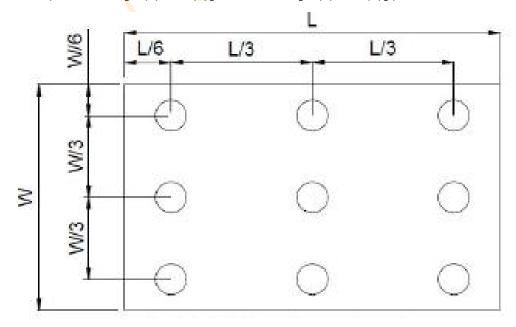
The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a windless room.



Note (6) Definition of White Variation ( $\delta$  W):

Measure the luminance of gray level 255 at 9 points

$$\delta$$
 W = (Minimum [L(1) ~ L(9)] / Maximum [L(1) ~ L(9)]) \*100%



# 8 Reliability Test

| Test Item                                       | Condition   |
|---|---|
| High Temperature Storage Test                   | 85°C, 240 hours   |
| Low Temperature Storage Test                    | -40°C, 240 hours  |
| High Temperature Operation Test                 | 80°C, 240 hours   |
| Low Temperature Operation Test                  | -30°C, 240 hours  |
| High temperature & high humidity operation Test | 60°C, 90%RH, 240 hours  |
|   | $-30$ °C/30 min $\sim +80$ °C/30 min, change time:5min, for a total |
| Thermal Shock(Non-operation)                    | 100 cycles, Start with cold temperature and end with high           |
|   | temperature.  |
| ESD   | Contact $\pm 4$ KV, Air $\pm 8$ KV                                  |
| Lob   | (R=330R,C=150pF)  |

- Note 1: There should be no condensation on the surface of panel during test.
- Note 2: Temperature of panel display surface area should be 85°C Max.
- Note 3: At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.
- Note 4: In the standard conditions, there is no function failure issue occurred. All the cosmetic specification is judged before reliability test.
- Note 5: Before cosmetic and function test, the product must have enough recovery time, at least 24 hours at room temperature.

## 9 Appearance Inspection

#### 9.1 Inspection Sampling Method:

Unless there is other agreement, the sampling plan for incoming inspection shall follow GB2828.1-2012.

- (1) Lot size: Quantity per shipment as one lot (different model as different lot).
- (2) Sampling type: Normal inspection, single sampling.
- (3) Sampling level: Level II.

#### 9.2 Inspection Conditions

The environmental condition and visual inspection shall be conducted as below:

- (1) Light: 800~1200Lux;
- (2) Vision requirement: Naked or corrected vision of at least 1.0 and no color blindness;
- (3) Inspection background: black and white board is used as the background below the product;
- (4) Inspection distance: 20~30cm from human eyes to the measured surface;
- (5) Inspection Angle: the detected surface is 90 degree from to line of sight, and the measured Object should be rotated 45 degree from side to side;
- (6) Inspection time: 5~10s;

#### 9.3 Inspection equipment

Secondary elements, calipers, feelers, filinka, petroleum ether, ND filter

#### 9.4 Defect code comparison table

| Code | Name (Unit)             | Code | Name (Unit)   |
|------|-------------------------|------|---------------|
| N    | Number                  | D    | Diameter (mm) |
| L    | Length (mm)             | Н    | Depth (mm)    |
| W    | Width (mm)              | S    | Distance (mm) |
| S    | Area (mm <sup>2</sup> ) |      |               |

#### 9.5 Inspection precautions

- (1) Inspectors must wear finger gloves and electrostatic bracelets.
- (2) Place the inspected product in front of the inspector, and hold the edge of the product with both hands carefully. Do not bend the product.

# 9.6 Product region division

Divide the products into nine parts. The center part is area A, and the surrounding part is area B, as shown in the figure below

| В | В | В |
|---|---|---|
| В | A | В |
| В | В | В |



# 9.7 Inspection Standards for Modules

# CTP and LCD fitting components

| Items              |  | Standard   | Conclusion   | Inspection tools |  |
|--------------------|--|--|--|------------------|--|
| D ( 1 1 1 1        |  | D≤0.15   | $D \le 0.15$ , it can be disregard.  |                  |  |
| Dot (white, black, |  | 0.15 / D / 0.5   | The distance with another dent or bubble is more   | -                |  |
| heterochromatic )  | A  | 0.15 <d≤0.5< td=""><td>than 15 mm, it allows to has two existence</td><td></td></d≤0.5<>           | than 15 mm, it allows to has two existence   |                  |  |
| b                  |  | D>0.3  | NG   | Visual           |  |
| a                  |  | D≤0.2  | D≤0.2, it can be disregard.  | inspection       |  |
|                    | В  | 0.2/D<0.5  | The distance with another dent or bubble is more   |                  |  |
| D=(a+b)/2          | Ь  | 0.2 <d≤0.5< td=""><td>than 15 mm, it allows to has three existence</td><td></td></d≤0.5<>          | than 15 mm, it allows to has three existence   |                  |  |
| D=(a+0)/2          |  | D>0.5  | NG   |                  |  |
|                    |  | W≤0.04   | $W \leq 0.04$ , it can be disregard.   |                  |  |
|                    |  | 0.04 <w≤0.06< td=""><td>L≤6, the distance with another scratch or impurity</td><td></td></w≤0.06<> | L≤6, the distance with another scratch or impurity   |                  |  |
| Linear defects     |  | $0.04 \le W \le 0.06$  | is more than 15 mm, it allows to has two existence   |                  |  |
| (fibers/foreign    | A  | 0.06 <w≤0.09< td=""><td>L≤6, the distance with another scratch or impurity</td><td></td></w≤0.09<> | L≤6, the distance with another scratch or impurity   |                  |  |
| bodies/ scratches, |  | 0.06 \ W \ 0.09  | is more than 15 mm, it allows to has one existence   |                  |  |
| etc.)              |  | W>0.09, L>6  | NG   | Visual           |  |
| <b>A</b> /         |  | W≤0.05   | W≤0.05, it can be disregard.   | inspection       |  |
|                    |  | 0.05 / W < 0.07  | L≤7, the distance with another scratch or impurity   | 1                |  |
| L                  | Ъ  | $0.05 < W \le 0.07$  | is more than 15 mm, it allows to has three existence   |                  |  |
| -                  | В  | 0.07 - W < 0.1   | L≤7, the distance with another scratch or impurity   |                  |  |
|                    |  | 0.07 \ W \ 0.1   | 0.07 <w<0.1 15="" allows="" existence<="" has="" is="" it="" mm,="" more="" td="" than="" to="" two=""></w<0.1>                      |                  |  |
|                    |  | W>0.125, L>8   | NG   |                  |  |
| Bubble             | A  | Zone A is judged a   | according to the point standard  | Visual           |  |
| Bubble             | В  | Zone B is judged a   | according to the point standard  | inspection       |  |
| Edge broken loss   |  |  |  |                  |  |
|                    | В  | is OK for reception  | e creaks, without damaging the line. The function test n, and the front side is not visible after fitting. front of cover plate: NG. | Visual           |  |
| Corner broken      |  |  | m; Y $\leq$ 1.5mm; Z $\leq$ GT(GT= glass thickness), without   | inspection       |  |
| loss               |  |  | ,  | пърссион         |  |
|                    | loss  Back A 13 min, 1 13 min, 2 401 (01 – glass thekness), without affecting the functionality of the product, each side is allowed one, no more than two in total. |  |  |                  |  |

| Items  |             | Standard Conclusion |  |                      |  |  |  |
|--|-------------|---------------------|--|----------------------|--|--|--|
| Creak  | A<br>+<br>B | Crea                | aks found at any location are not allowed  |                      |  |  |  |
| Newton's rings   | A           |                     | o more than 1/3 of the display area, light up invisible. If there are ecial requirements, the limited sample shall prevail.  |                      |  |  |  |
| Panel color  |             | Consistent with th  | e sample, no serious color difference  |                      |  |  |  |
| Size   |             | Meet the requirem   | ents of finished product inspection drawings   |                      |  |  |  |
| Ink pinhole  | В           | D≤0.25              | Distance of two pinholes greater than 15mm, the paint can be repaired with a paint pen of the same color as the cover plate. | Visual<br>inspection |  |  |  |
| Ink sawtooth   | A<br>+<br>B | W≤0.2, L≤1          | The distance between two serrations near the VA area is greater than or equal to 50mm, two serrations may be allowed.        |                      |  |  |  |
| Circular White Mura, Lumination Mura, Black/White Mura, etc. | A           | Refer to 5% ND fi   | lter in 50% Should not be perceived, If needed or  | Visual inspection    |  |  |  |

#### 10 Notices

#### 10.1Cautions for storage

Store the module in a dark room where must keep at 25±10°C and 65%RH or less. Don't expose the products to the direct sunlight or stresses.

#### 10.2 Cautions for operation

- (1) Do not put a heavy, hard or sharp object on the product.
- (2) Do not bend the product in order to assure the reliability.
- (3) Do not put one product on the other. Otherwise, the product to may be scratched or be changed.
- (4) Do not oile the touch panel, Do not put heavy goods on the touch panel.
- (5) Do not use any organic solvent acid or alkali solution to clean the surface of the product. Please use dry clothes or soft clothes with ethanol to clean the surface.

#### 10.3 Cautions for operation

Light transmission is an important factor for the product. So, please wear clean finger sacks, gloves and mask to protect the products from fingerprint or stain attach, and also must ensure the area where your finger touches is outside the view area when handing the panel.

#### 10.4 Others

- (1) Please note that dew gathering in the panel due to abrupt temperature or humidity change, etc. Which may cause deterioration of performance.
- (2) When this product was built into the package, if there is vulcanization material such as vulcanized rubber which has a possibility of generating the salutation gas near the package since abnormalities will be caused to wring of the product and it will become the cause if functional degradation, please give a constitutional cations.
- (3) Cation for product safety set

Although full care is taken to ensure product quality, failure modes such as degradation, short circuits, or open circuits might be caused, Therefore, to design a product set, please study the effects of any single failure of the panel in advance and consider the safety of product configuration.

Quality function livers for on year, outward appearance haves non-color variation in six months.

#### **Disclaimer**

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