晶采光電科技股份有限公司
AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-320240LATZQW-00H
APPROVED BY	
DATE	

☑ **Approved For Specifications**
☐ **Approved For Specifications & Sample**

AMPIRE CO., LTD.
Building A., 4F., No.116, Sec. 1, Sintai 5th Rd., Xizhi Dist,
New Taipei City 221, Taiwan (R.O.C.)
新北市汐止區新台五路一段 **116** 號 **4** 樓**(**東方科學園區 **A** 棟**)**
TEL:886-2-26967269 , FAX:886-2-26967196 or 26967270

APPROVED BY	CHECKED BY	ORGANIZED BY

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2012/5/02	--	New Release.	Leo
2012/9/21	15	Revise the viewing angle.	Leo

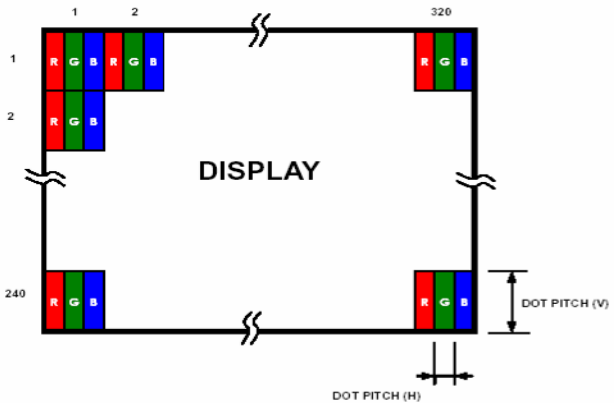
1. General Description and Features

3.5 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 3.5" TFT-LCD panel, a driver circuit and backlight unit.

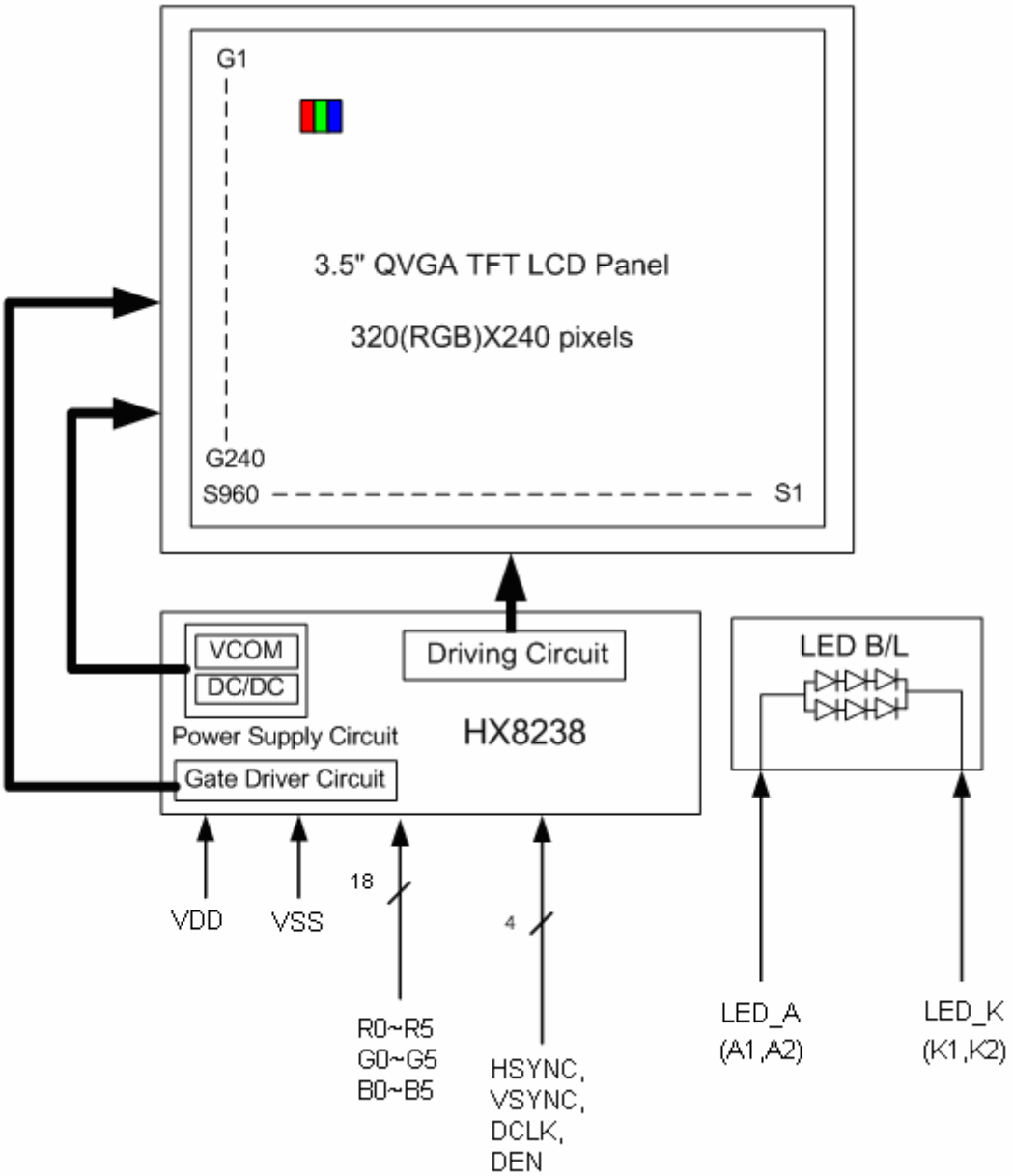
1.1 Construction: 3.5" a-Si color TFT-LCD, White LED Backlight and PCB.
1.2 Resolution (pixel): 320(R.G.B) X240.
1.3 Number of the Colors: 262K colors (R, G, B 6 bit digital each).
1.4 LCD type: Transmissive Color TFT LCD (normally White).
1.5 View Angle: 12 o'clock
1.6 18-Bit RGB Interface.
1.7 Interface: 40 pin.
1.8 Support SYNC Mode Only.
1.9 Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.
1.10 LED Type Backlight.
1.11 EVA for wide viewing angle, no gray inversion

2. Physical specifications

Item	Specifications	unit
Display Resolution	320(W) x 240(H)	dot
Active area	70.08 x 52.56	mm
Screen size	3.5(Diagonal)	inch
Dot pitch	0.073 (W) x 0.219 (H)	mm
Color configuration	R.G.B – stripe	
Overall Dimension	77.8(W) x 66.0(H) x 4.4(T)	mm
Input interface	digital 18-bits RGB	
Surface Treatment	Anti - glare(AG)	
Backlight unit	White LED	
Display Mode	Normally White/Transmissive	



3. Functional Block Diagram



4. Electrical Specifications

TFT LCD Panel FPC Descriptions

Pin no	Symbol	I/O	Description	Remark
1~4	VDD		Power supply for the logic (3.3V)	
5	Hsync	I	Horizontal sync input in digital RGB mode	
6	DEN		Keep this Pin NC.	
7	VSS		GND	
8	DCLK	I	Clock signal. Latching data at the rising edge.	
9	VSS		GND	
10	Vsync	I	Vertical sync input in digital RGB mode.	
11	VSS		GND	
12	B5	I	Blue data	
13	B4	I		
14	B3	I		
15	VSS		GND	
16	B2	I	Blue data	
17	B1	I		
18	B0	I		
19	VSS		GND	
20	G5	I	Green data	
21	G4	I		
22	G3	I		
23	VSS		GND	
24	G2	I	Green data	
25	G1	I		
26	G0	I		
27	VSS		GND	
28	R5	I	Red data	
29	R4	I		
30	R3	I		
31	VSS		GND	
32	R2	I	Red data	
33	R1	I		
34	R0	I		
35	NC	I	Not use	
36	VSS		GND	
37	YU / A1	I	1.Backlight AK pin(without touch panel function) 2.Touch panel control pin(Backlight AK are driven by LED Driver circuit)	
38	XL / A2	I		
39	YD / K1	I		
40	XR / K2	I		

5. Basic Display Color and Gray Scale

	Color & Gray Scale	DATA SIGNAL																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(1)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(0)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(61)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(1)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(0)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

6. Absolute Maximum Ratings

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

6.1 Environmental Absolute max. ratings

Item	OPERATING		STORAGE		Remark
	MIN	MAX	MIN	MAX	
Temperature	-20	70	-30	80	Note2,3,4,5,6,7,8
Humidity	Note1		Note1		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note1 : Ta <= 40℃ : 85% RH max

Ta > 40℃: Absolute humidity must be lower than the humidity of 85%RH at 40℃

Note2 : For storage condition Ta at -30℃ < 48h , at 80℃ < 100h

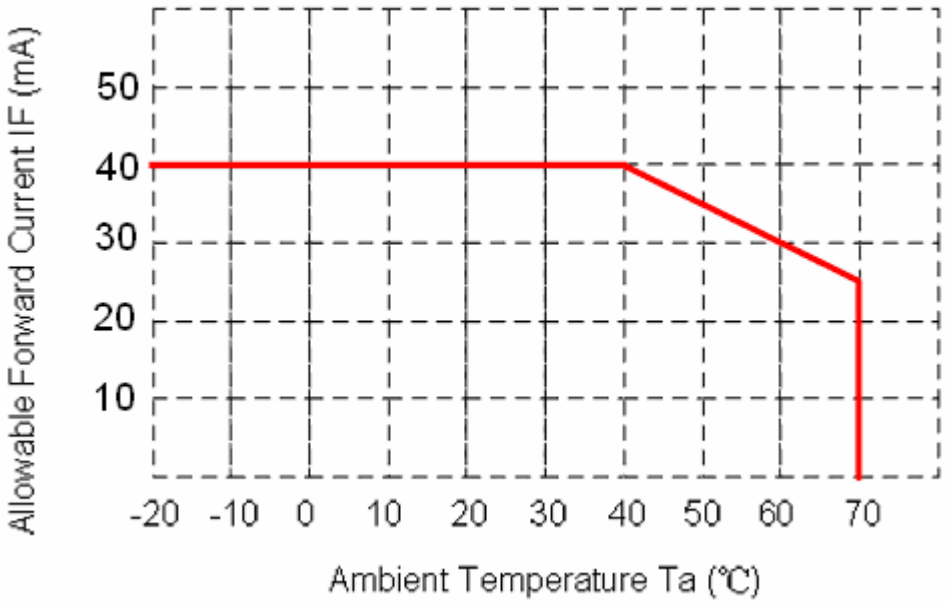
For operating condition Ta at -20℃ < 100h

Note3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note4 : The response time will be slower at low temperature.

Note5 : Only operation is guarantied at operating temperature. Contrast, response time, another display quality are evaluated at +25℃

Note6 : When LCM is operated over 40℃ ambient temperature, the I_{LED} of the LED back-light should be follow :



Note7 : This is panel surface temperature, not ambient temperature.

Note8 : When LCM be operated over than 40℃ , the life time of the LED back-light will be reduced.

6.2 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VDD	VSS=0	-0.3	6.0	V	
Input voltage	V_{in}		-0.3	VDD+0.3	V	Note 1

Note1:Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

7. Electrical Characteristics

7.1 DC Electrical characteristic of the LCD

Typical operating conditions (VSS=0V)

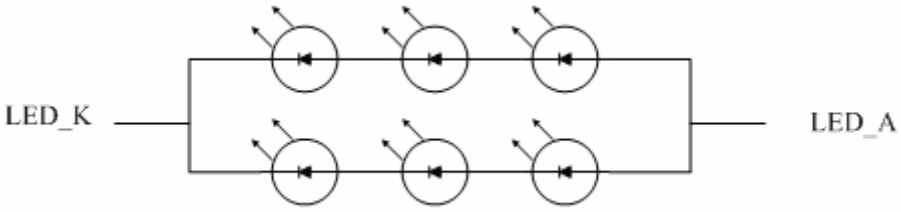
Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply		VDD	3.0	3.3	3.6	V	
Input Voltage for logic	H Level	V_{IH}	0.7 VDD	-	VDD	V	Note 1
	L Level	V_{IL}	0	-	0.3 VDD	V	
Power Supply current		IDD		**15**	--	mA	Note 2

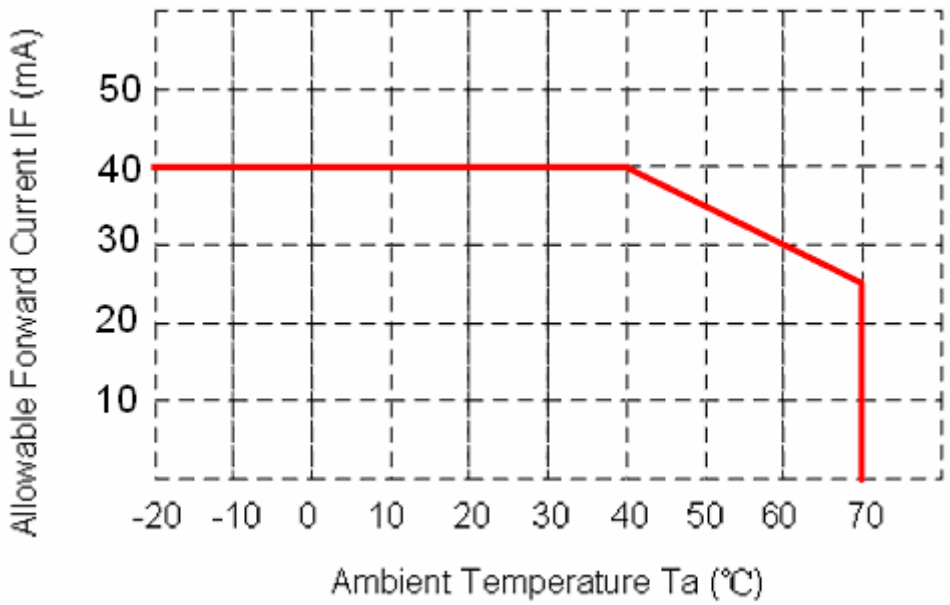
Note1: Hsync, Vsync, DEN, DCLK, R0~R5, G0~G5, B0~B5

Note2: fv =60Hz, Ta=25℃, Display pattern: All Black

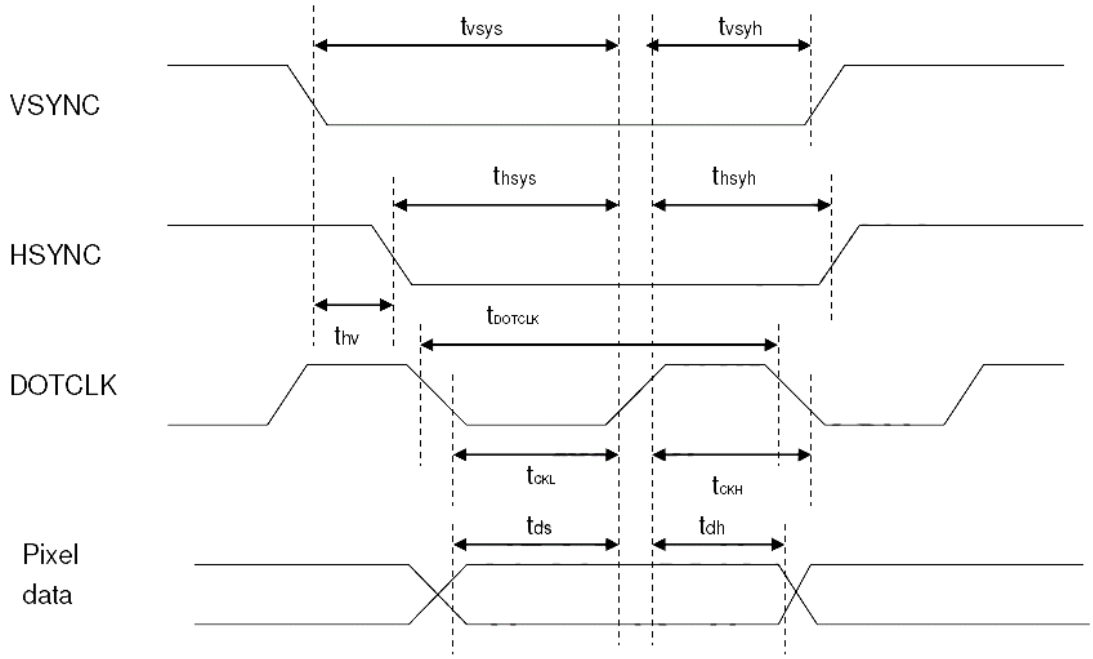
7.2 Electrical characteristic of LED Back-light

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
LED voltage	V_{AK}	9.6	-	11	V	I_{LED} =40mA,Ta=25℃
LED forward current	I_{LED}	--	40	-	mA	Ta=25℃
	I_{LED}	--	30	-	mA	Ta=60℃
LED life time			15k		Hr	Ta=25℃ I_{LED} =40mA





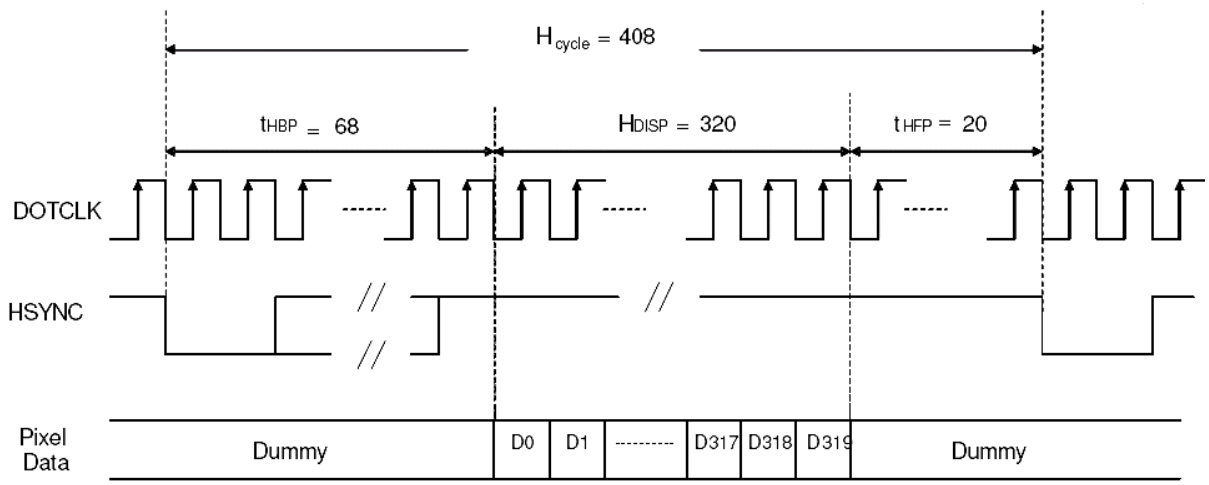
8. AC Timing characteristic of the LCD



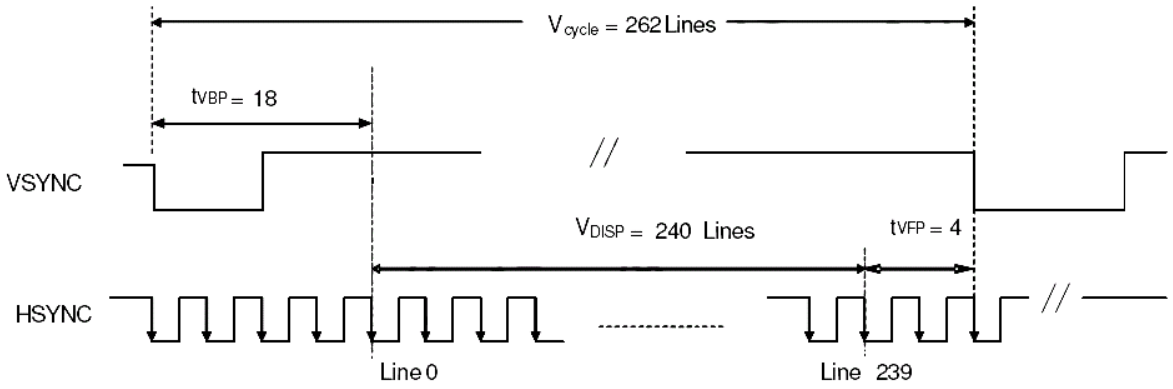
Characteristics	Symbol	Min		Typ		Max		Unit
		24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency	fDOTCLK	-		6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-		ns
Vertical Sync Setup Time	tvsys	20	10	-		-		ns
Vertical Sync Hold Time	tvsyh	20	10	-		-		ns
Horizontal Sync Setup Time	thsys	20	10	-		-		ns
Horizontal Sync Hold Time	thsyh	20	10	-		-		ns
Phase difference of Sync Signal Falling Edge	thv	1		-		240		tDOTCLK
DOTCLK Low Period	tCKL	50	15	-		-		ns
DOTCLK High Period	tCKH	50	15	-		-		ns
Data Setup Time	tds	12	10	-		-		ns
Data hold Time	tdh	12	10	-		-		ns
Reset pulse width	tRES	10		-		-		us

Note: External clock source must be provided to DOTCLK pin of HX8238-A. The driver will not operate if absent of the clocking signal.

Pixel Timing Table

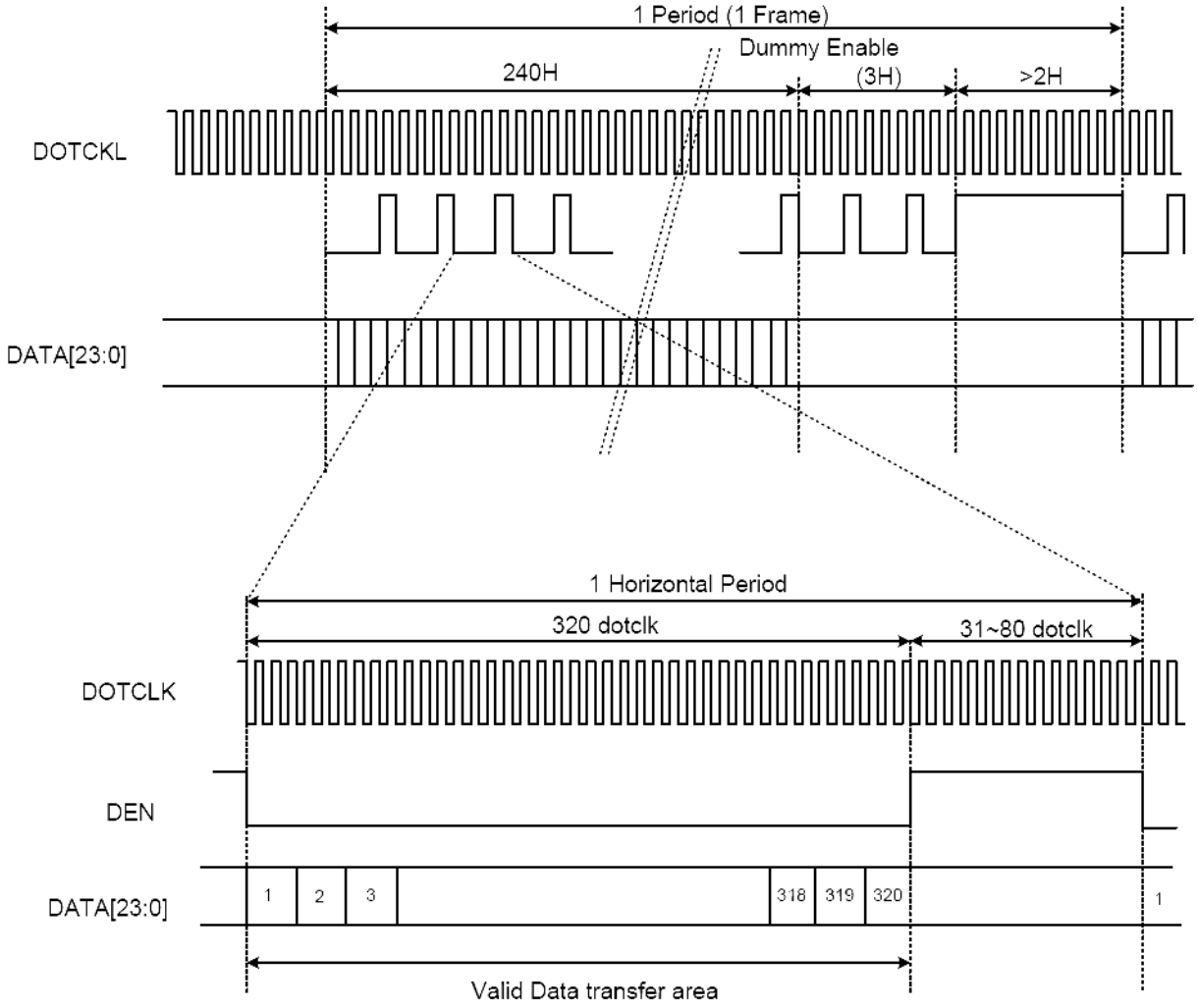


a) Horizontal Data Transaction Timing



b) Vertical Data Transaction Timing

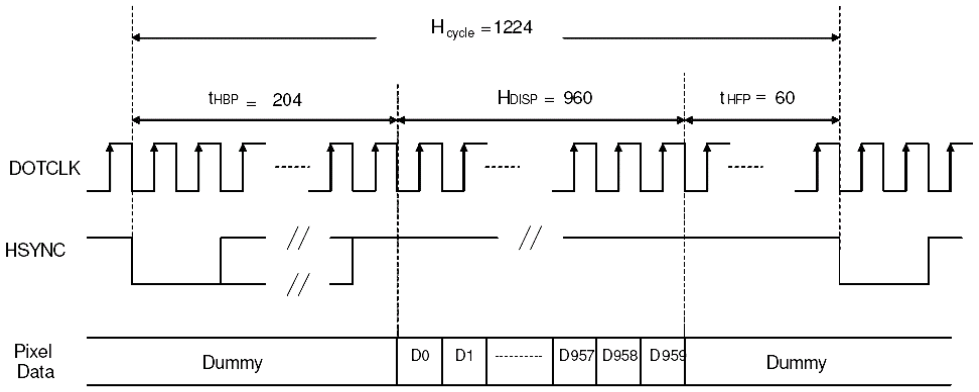
(a) Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)



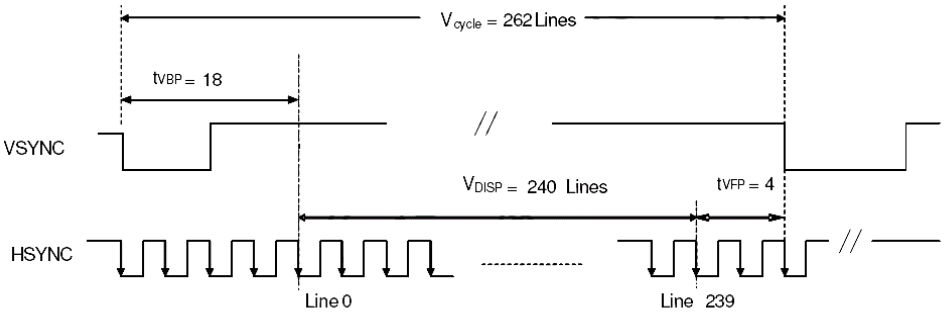
b) Data Transaction Timing in Parallel RGB (24 bit) Interface (DE Mode)

Characteristics		Symbol	Min		Typ		Max		Unit
			24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency		fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period		tDOTCLK	100	33.3	154	51.3	-	-	ns
Horizontal Frequency (Line)		fH	-		14.9		22.35		KHz
Vertical Frequency (Refresh)		fV	-		60		90		Hz
Horizontal Back Porch		tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Front Porch		tHFP	-	-	20	60	-	-	tDOTCLK
Horizontal Data Start Point		tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Blanking Period		tHBP + tHFP	-	-	88	264	-	-	tDOTCLK
Horizontal Display Area		HDISP	-	-	320	960	-	-	tDOTCLK
Horizontal Cycle		Hcycle	-	-	408	1224	450	1350	tDOTCLK
Vertical Back Porch		tVBP	-		18		-		Lines
Vertical Front Porch		tVFP	-		4		-		Lines
Vertical Data Start Point		tVBP	-		18		-		Lines
Vertical Blanking Period		tVBP + tVFP	-		22		-		Lines
Vertical Display Area	NTSC	VDISP	-		240		-		Lines
	PAL				280(PALM=0)				
					288(PALM=1)				
Vertical Cycle	NTSC	Vcycle	-		262		350		Lines
	PAL				313				

Data Transaction Timing in Normal Operating Mode



a) Horizontal Data Transaction Timing



b) Vertical Data Transaction Timing

Data Transaction Timing in Serial RGB (8 bit) Interface (SYNC Mode)

9. Optical specification

9.1 Optical characteristic of the LCD

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response Time		T_r+T_f	Θ=0°		50	80	ms	Note 1,2,3,5
Contrast ratio		CR	At optimized viewing angle	-	300	-		Note 1,2,4,5
Viewing Angle	Top Bottom Left Right		CR≧10	75 75 75 75	85 85 85 85	- - - -	deg.	Note1,2, 5,6
Brightness		Y_L	I_{LED}=40mA ,25℃	-	350	-	cd/m^2	Note 7
White chromaticity		Xw		0.25	-	0.35		
		Yw		0.26	-	0.36		

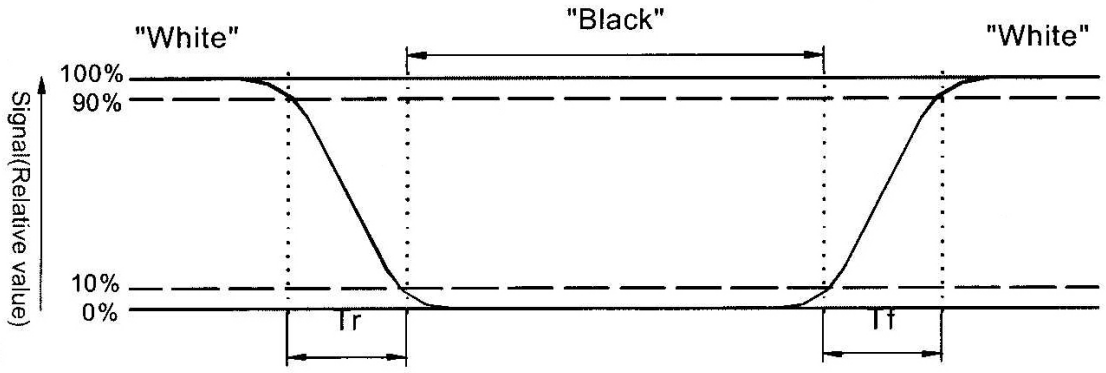
( )For reference only. These data should be update according the prototype.

Note 1: Note 1: Ambient temperature=25℃, and lamp current I_{LED}=20mA.To be measured in the dark room.

Note 2: To be measured on the center area of panel with a viewing cone of 1°by Topcon luminance meter BM-7, after 10 minutes operation.

Note 3.Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black“ to “white”(falling time) and from ”white” to “black” (rising time),respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4.Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note 5: White $V_i = V_{i50} + 1.5V$

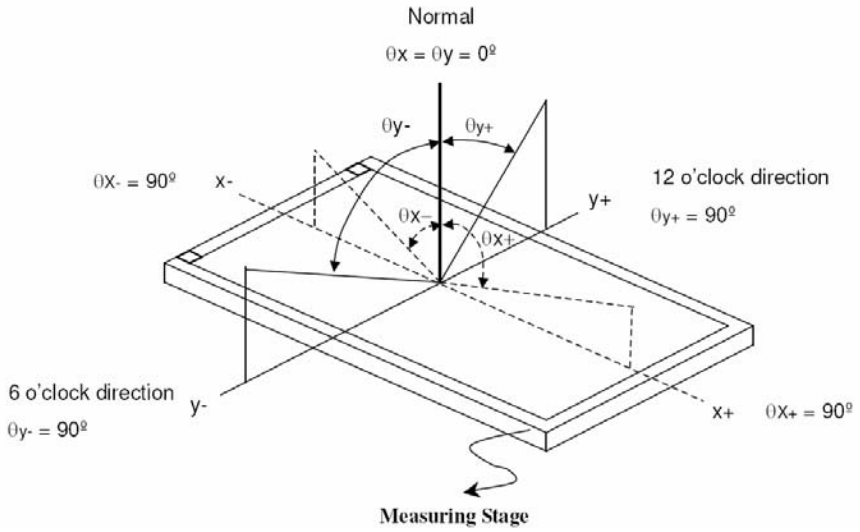
Black $V_i = V_{i50} + 2.0V$

"±"means that the analog input signal swings in phase with V_{COM} signal.

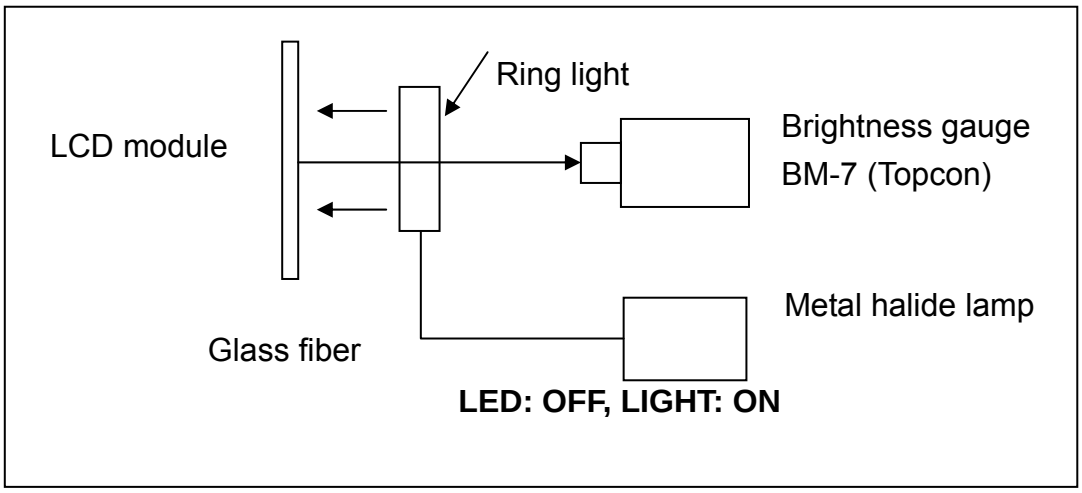
"$\frac{-}{+}$" means that the analog input signal swings out of phase with V_{COM} signal.

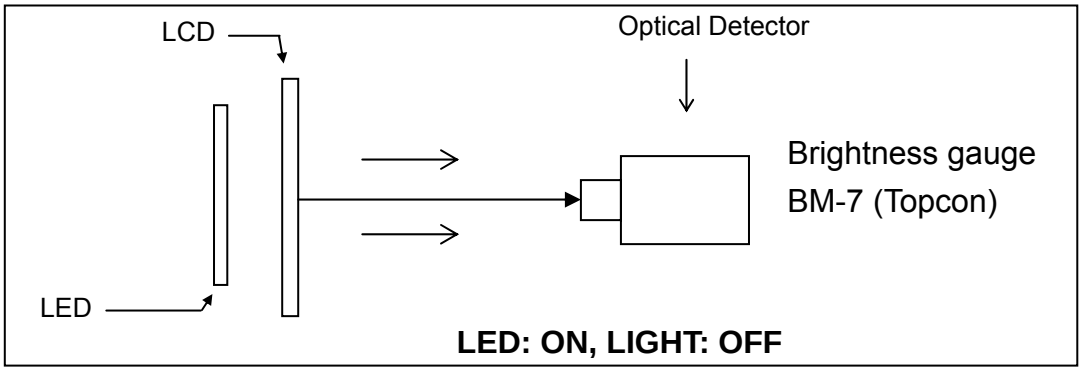
V_{i50} : The analog input voltage when transmission is 50%.The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6.Definition of viewing angle, Refer to figure as below.



Note 7.Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.





10. Inspection Specifications

10.1. Scope

Specifications contain

1.1 Display Quality Evaluation

1.2 Mechanics Specification

10.2. Sampling Plan

Unless there is other agreement, the sampling plan for incoming inspection shall follow MIL-STD-105E LEVEL II.

2.1 Lot size: Quantity per shipment as one lot (different model as different lot).

2.2 Sampling type: Normal inspection, single sampling.

2.3 Sampling level: Level II.

2.4 AQL: Acceptable Quality Level

Major defect: AQL=0.65

Minor defect: AQL=1.0

10.3. Panel Inspection Condition

3.1 Environment:

Room Temperature: 25±5°C.

Humidity: 65±5% RH.

Illumination: 300 ~ 700 Lux.

3.2 Inspection Distance:

35-40 cm

3.3 Inspection Angle:

The vision of inspector should be perpendicular to the surface of the Module.

3.4 Inspection time：

Perceptibility Test Time: 20 seconds max.

10.4. Display Quality

4.1 Function Related:

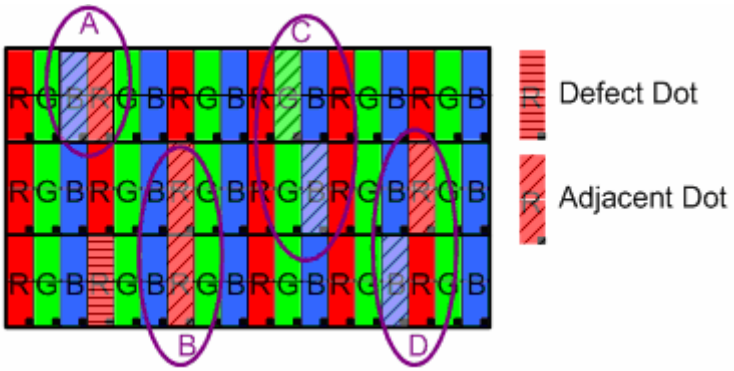
The function defects of line defect, abnormal display, and no display are considered Major defects.

4.2 Bright/Dark Dots:

Defect Type / Specification	G0 Grade	A Grade
Bright Dots	0	N≤ 1
Dark Dots	0	N≤ 3
Total Bright and Dark Dots	0	N≤ 3

[Note 1]

Judge defect dot and adjacent dot as following.



(1) One pixel consists of 3 sub-pixels, including R,G, and B dot.(Sub-pixel = Dot)

(2) The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.

(3) Allow above (as A, B, C and D status) adjacent defect dots, including bright and dart adjacent dot. And they will be counted 2 defect dots in total quantity.

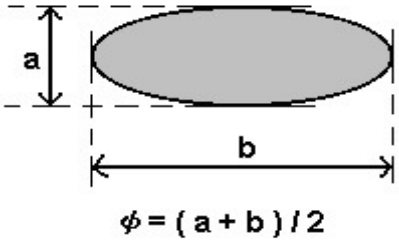
(4) Defects on the Black Matrix, out of Display area, are not considered as a defect or counted.

(5) There should be no distinct non-uniformity visible through 6% ND Filter within 2 sec inspection times.

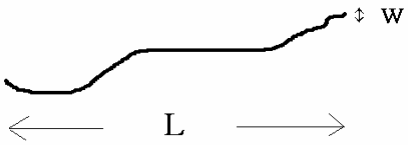
4.3 Visual Inspection specifications:

Defect Type	Specification	Count(N)
Dot Shape (Particle、Scratch and Bubbles in display area)	D≤0.15mm	Ignored
	0.15mm < D≤ 0.3mm	N≤ 3
	D > 0.3mm	N=0
Line Shape (Particles、Scratch、Lint and Bubbles in display area)	W≤ 0.05mm	Ignored
	0.05mm<W≤ 0.1mm ， L≤ 3mm	N≤ 3
	W > 0.1mm ， L > 3mm	N=0

[Note 2] W: Width [mm], L: Length [mm], N: Number, φ: Average Diameter

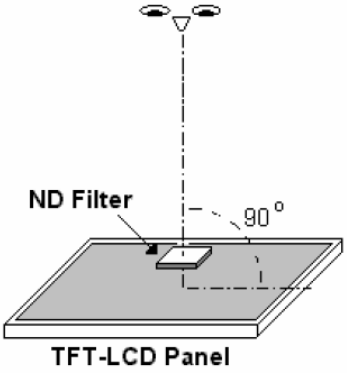


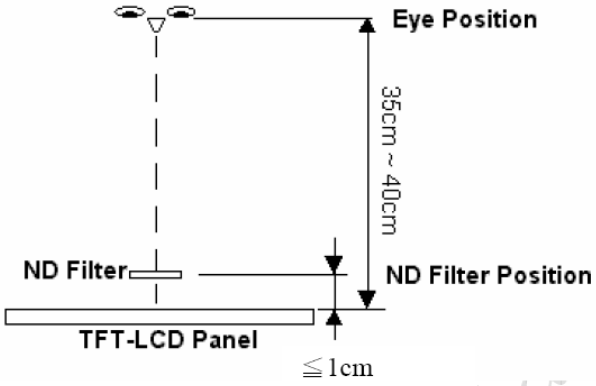
1. (White, black) Spot
2. Polarizer Bubble



1. fiber

[Note 3] Bright dot is defined through 6% transmission ND Filter as following.





11. USE PRECAUTIONS

11.1 Handling precautions

1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

1) To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

11.4 Operating precautions

1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

11.5 Other

1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

12. Mechanical Dimensions